



P-Ch 100V Fast Switching MOSFETs

Description

The HSU0115 uses advanced trench MOSFET technology to provide excellent $R_{DS(ON)}$ and gate charge for use in a wide variety of other applications.

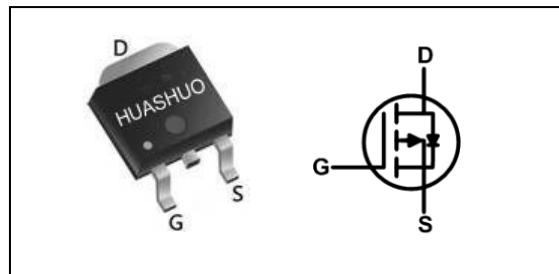
The HSU0115 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

- 100% EAS Guaranteed
- Green Device Available
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

Product Summary

V_{DS}	-100	V
$R_{DS(ON),typ}$	78	mΩ
I_D	-18	A

TO252 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_c=25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V_1$	-18	A
$I_D@T_c=100^\circ C$	Continuous Drain Current, $V_{GS} @ -10V_1$	-12	A
I_{DM}	Pulsed Drain Current ²	-75	A
EAS	Single Pulse Avalanche Energy ³	157.2	mJ
I_{AS}	Avalanche Current	18.9	A
$P_D@T_c=25^\circ C$	Total Power Dissipation ⁴	54	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ₁	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ₁	---	2.3	°C/W



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Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_D=-250\mu\text{A}$	-100	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$\text{V}_{\text{GS}}=-10\text{V}$, $\text{I}_D=-10\text{A}$	---	78	95	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=-4.5\text{V}$, $\text{I}_D=-8\text{A}$	---	86	110	
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$, $\text{I}_D=-250\mu\text{A}$	-1.2	-1.7	-2.5	V
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=-100\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $\text{T}_J=25^{\circ}\text{C}$	---	---	-50	μA
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$\text{V}_{\text{DS}}=-10\text{V}$, $\text{I}_D=-10\text{A}$	---	24	---	S
Q_{g}	Total Gate Charge	$\text{V}_{\text{DS}}=-50\text{V}$, $\text{V}_{\text{GS}}=-10\text{V}$, $\text{I}_D=-20\text{A}$	---	44.5	---	nC
Q_{gs}	Gate-Source Charge		---	9.13	---	
Q_{gd}	Gate-Drain Charge		---	5.93	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$\text{V}_{\text{DD}}=-50\text{V}$, $\text{V}_{\text{GS}}=-10\text{V}$, $\text{R}_G=3.3\Omega$, $\text{I}_D=-10\text{A}$	---	12	---	ns
T_r	Rise Time		---	27.4	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	79	---	
T_f	Fall Time		---	53.6	---	
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=-20\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	3029	---	pF
C_{oss}	Output Capacitance		---	129	---	
C_{rss}	Reverse Transfer Capacitance		---	76	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,5}	$\text{V}_G=\text{V}_D=0\text{V}$, Force Current	---	---	-18	A
V_{SD}	Diode Forward Voltage ²	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_s=-1\text{A}$, $\text{T}_J=25^{\circ}\text{C}$	---	---	-1.2	V
t_{rr}	Reverse Recovery Time	$\text{I}_F=-8\text{A}$, $\text{di}/\text{dt}=-100\text{A}/\mu\text{s}$,	---	38.7	---	nS
Q_{rr}	Reverse Recovery Charge	$\text{T}_J=25^{\circ}\text{C}$	---	22.4	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch²FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $\text{V}_{\text{DD}}=-25\text{V}$, $\text{V}_{\text{GS}}=-10\text{V}$, $\text{L}=0.88\text{mH}$, $\text{I}_{\text{AS}}=-18.9\text{A}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.



Typical Characteristics

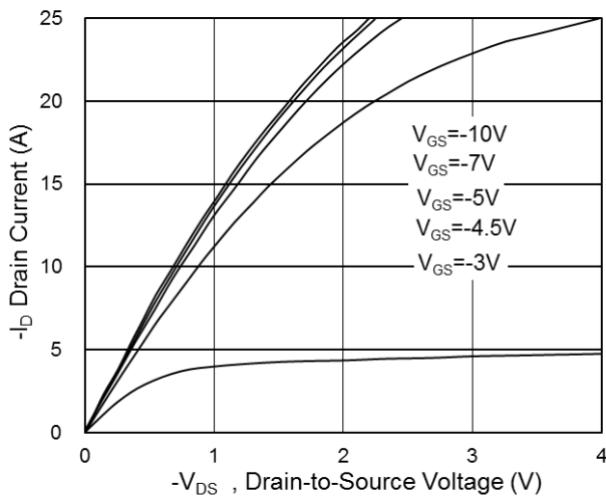


Fig.1 Typical Output Characteristics

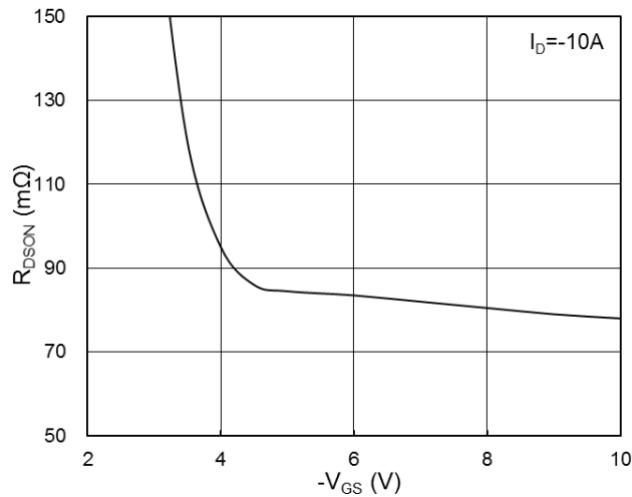


Fig.2 On-Resistance vs. G-S Voltage

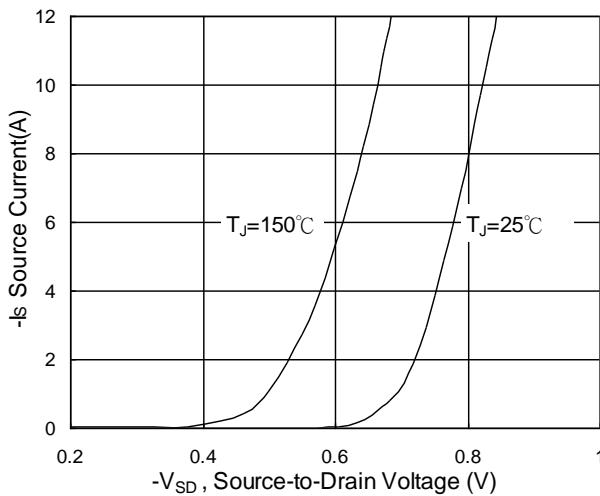


Fig.3 Typical S-D Diode Forward Voltage

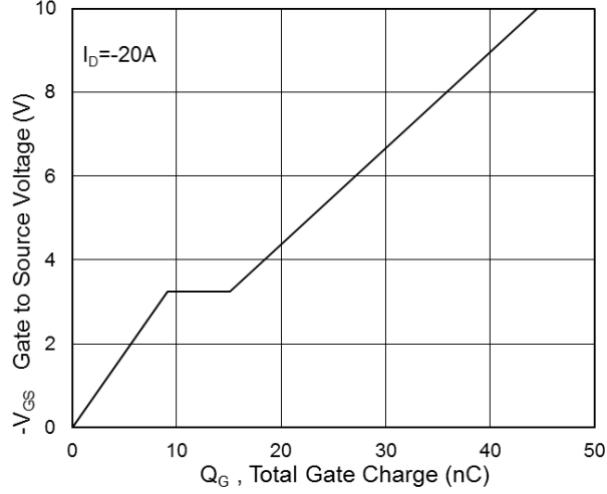


Fig.4 Gate-Charge Characteristics

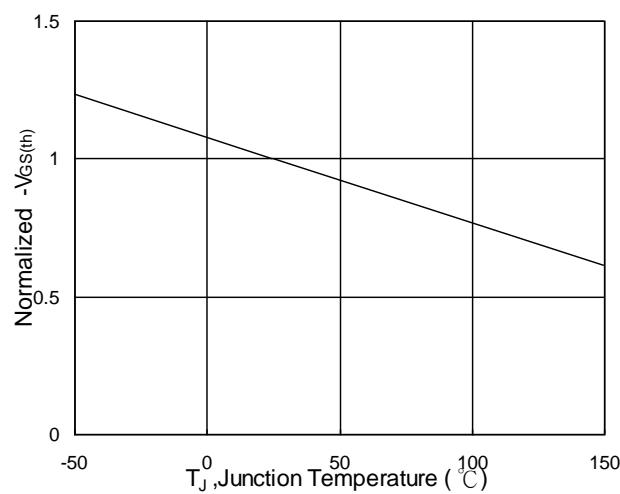


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

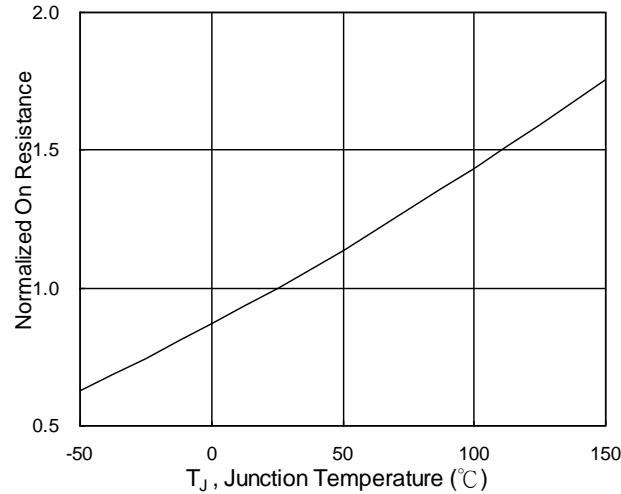


Fig.6 Normalized $R_{DS(on)}$ vs. T_J



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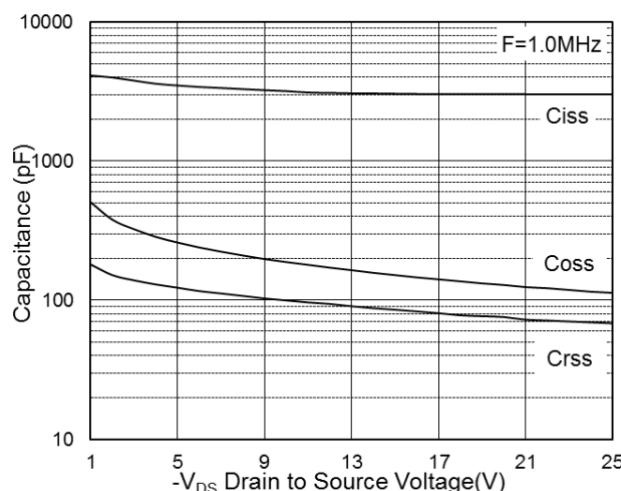


Fig.7 Capacitance

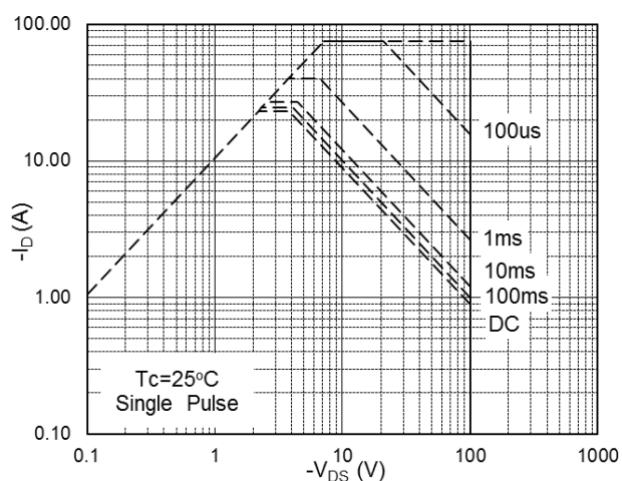


Fig.8 Safe Operating Area

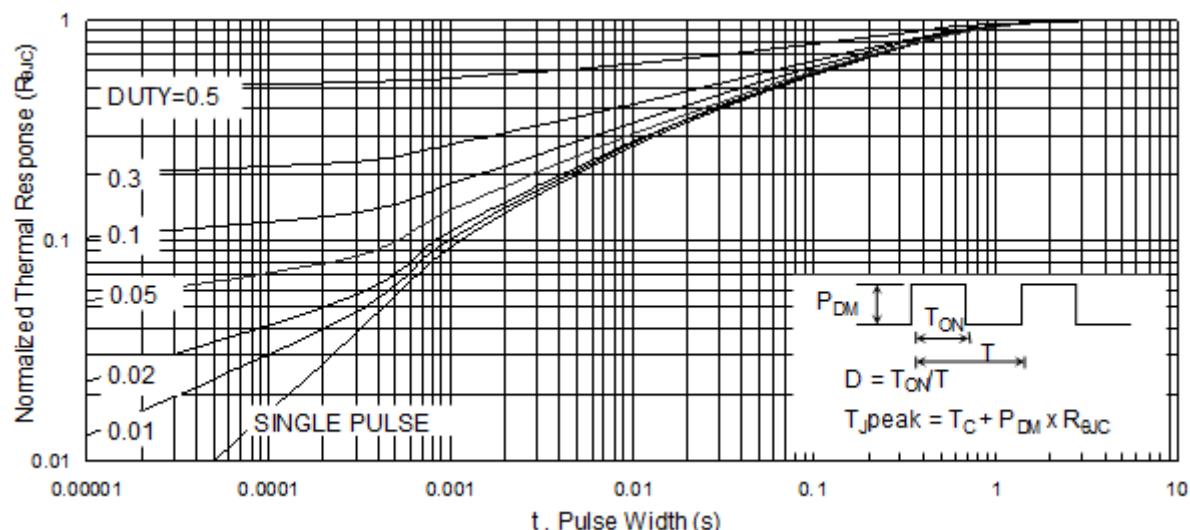


Fig.9 Normalized Maximum Transient Thermal Impedance

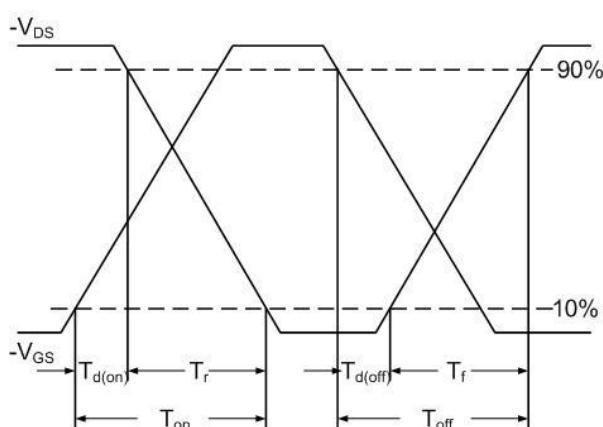


Fig.10 Switching Time Waveform

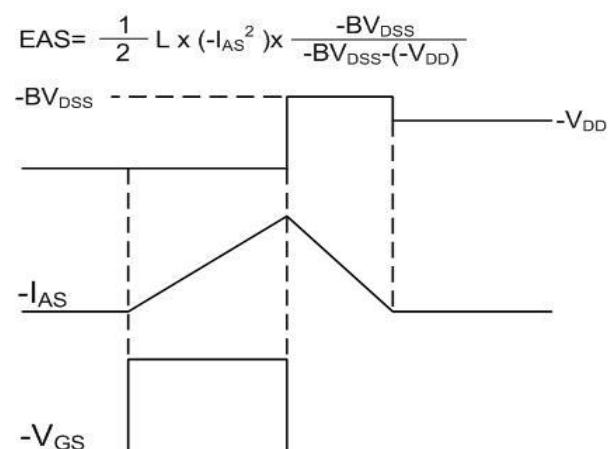
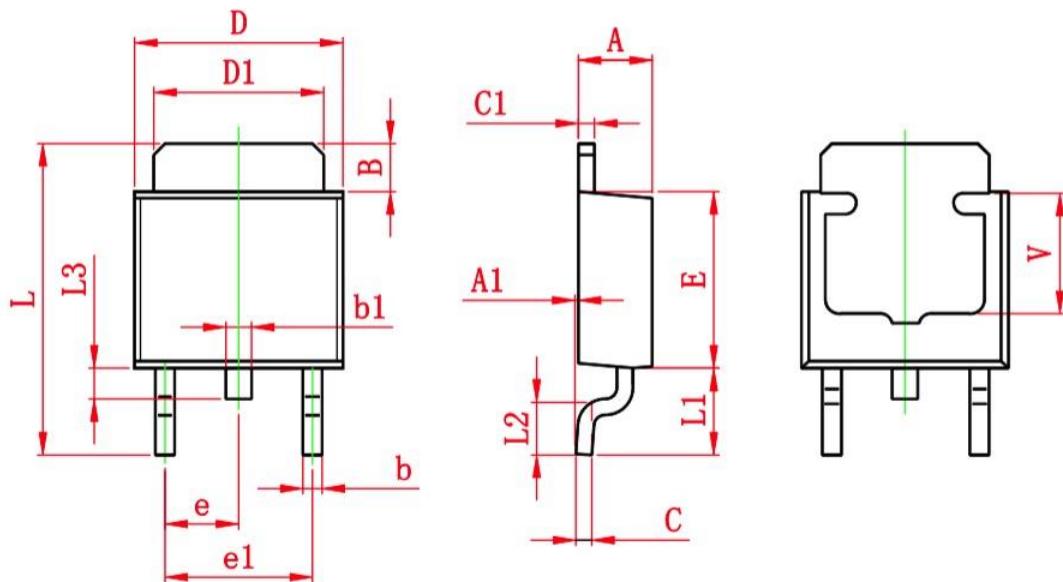


Fig.11 Unclamped Inductive Waveform



Ordering Information

Part Number	Package code	Packaging
HSU0115	TO252-2	2500/Tape&Reel



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
B	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
c	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	6.350	6.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.400	5.700	0.213	0.224
e	2.300 TYP.		0.091 TYP.	
e1	4.500	4.700	0.177	0.185
L	9.500	9.900	0.374	0.390
L1	2.550	2.900	0.100	0.114
L2	1.400	1.780	0.055	0.070
L3	0.600	0.900	0.024	0.035
V	3.800 REF.		0.150 REF.	