

Description

The HSM0040 is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

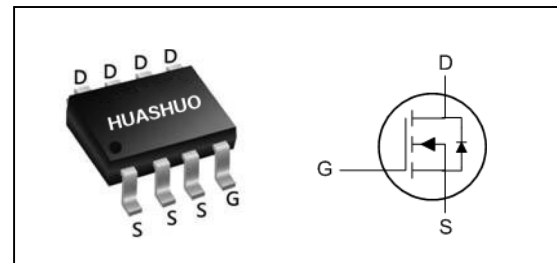
The HSM0040 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

- 100% EAS Guaranteed
- Green Device Available
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

Product Summary

V_{DS}	100	V
$R_{DS(ON),max}$	14	mΩ
I_D	11	A

SOP8 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	±20	V
$I_D@T_A=25^{\circ}C$	Continuous Drain Current, V_{GS} @ 10V ¹	11	A
$I_D@T_A=70^{\circ}C$	Continuous Drain Current, V_{GS} @ 10V ¹	8	A
I_{DM}	Pulsed Drain Current ²	50	A
EAS	Single Pulse Avalanche Energy ³	101	mJ
I_{AS}	Avalanche Current	45	A
$P_D@T_A=25^{\circ}C$	Total Power Dissipation ⁴	3.1	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹ ($t \leq 10s$)	---	40	°C/W
	Thermal Resistance Junction-Ambient ¹	---	75	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	24	°C/W



Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=10A$	---	---	14	m Ω
	Static Drain-Source On-Resistance ²	$V_{GS}=4.5V, I_D=8A$	---	---	17	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	---	2.4	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=80V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{DS}=80V, V_{GS}=0V, T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5V, I_D=10A$	---	43	---	S
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	---	0.7	---	Ω
Q_g	Total Gate Charge (10V)	$V_{DS}=80V, V_{GS}=10V, I_D=10A$	---	75	---	nC
Q_{gs}	Gate-Source Charge		---	15.5	---	
Q_{gd}	Gate-Drain Charge		---	20.3	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=40V, V_{GS}=10V, R_G=3.3\Omega, I_D=10A$	---	18.5	---	ns
T_r	Rise Time		---	8.8	---	
$T_{d(off)}$	Turn-Off Delay Time		---	58.8	---	
T_f	Fall Time		---	15.8	---	
C_{iss}	Input Capacitance	$V_{DS}=25V, V_{GS}=0V, f=1\text{MHz}$	---	4708	---	pF
C_{oss}	Output Capacitance		---	326	---	
C_{rss}	Reverse Transfer Capacitance		---	247	---	

Diode Characteristics

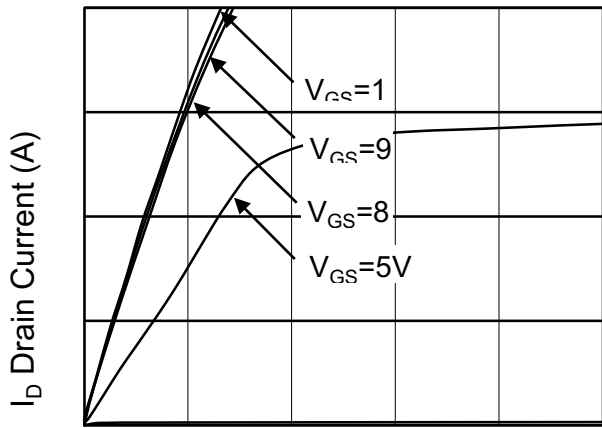
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ^{1,5}	$V_G=V_D=0V, \text{Force Current}$	---	---	11	A
I_{SM}	Pulsed Source Current ^{2,5}		---	---	50	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_S=A, T_J=25^\circ\text{C}$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$I_F=10A, dI/dt=100A/\mu s,$	---	28	---	nS
Q_{rr}	Reverse Recovery Charge	$T_J=25^\circ\text{C}$	---	50	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=45A$
- 4.The power dissipation is limited by 150 $^\circ\text{C}$ junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

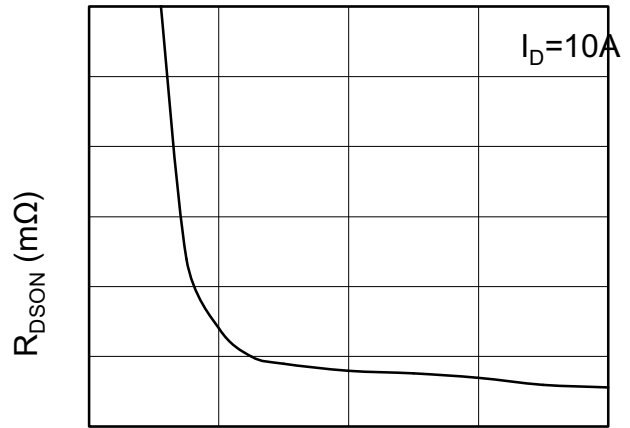


Typical Characteristics



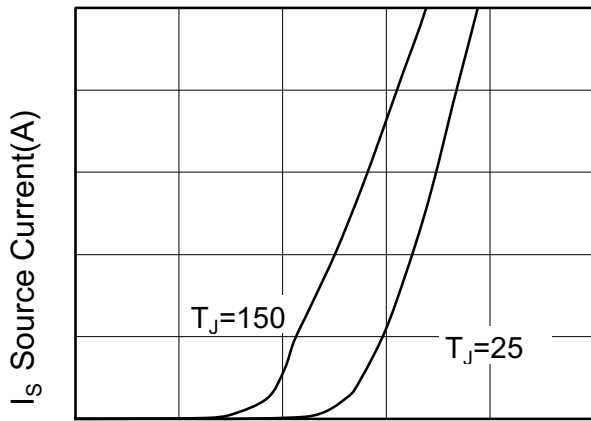
V_{DS} , Drain-to-Source Voltage (V)

Fig.1 Typical Output Characteristics



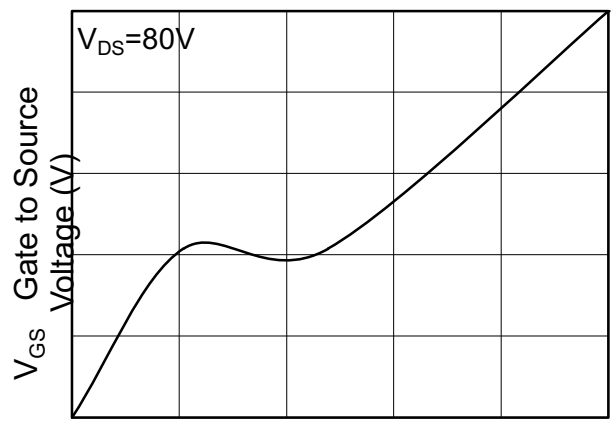
V_{GS} (V)

Fig.2 On-Resistance v.s Gate-Source



V_{SD} , Source-to-Drain Voltage (V)

Fig.3 Forward Characteristics of Reverse



Q_G , Total Gate Charge (nC)

Fig.4 Gate-Charge Characteristics

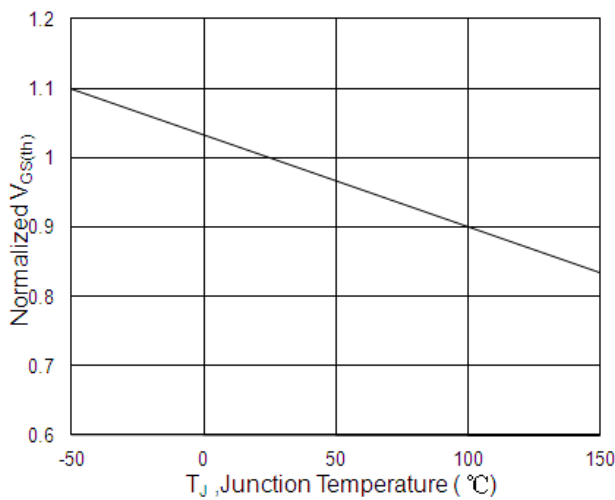


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

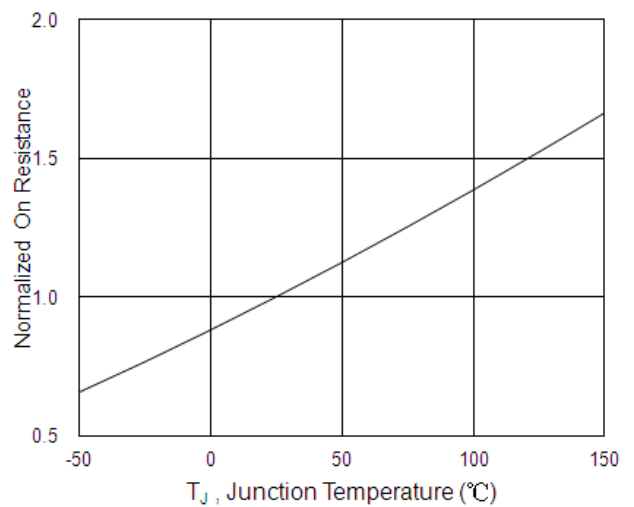


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

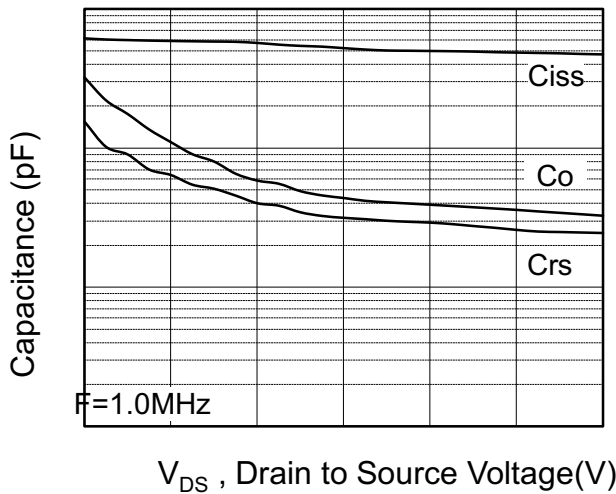


Fig.7 Capacitance

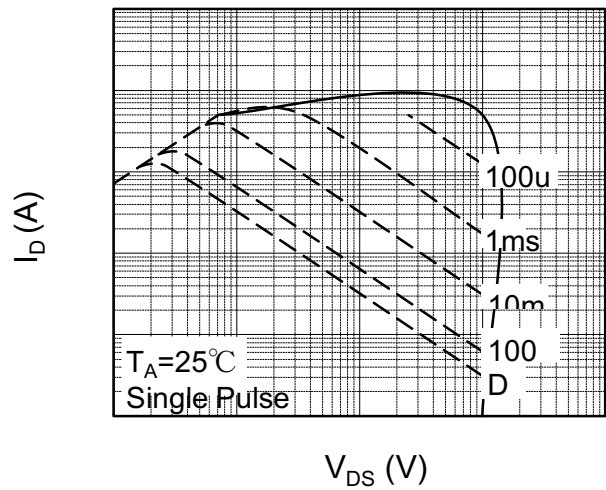


Fig.8 Safe Operating Area

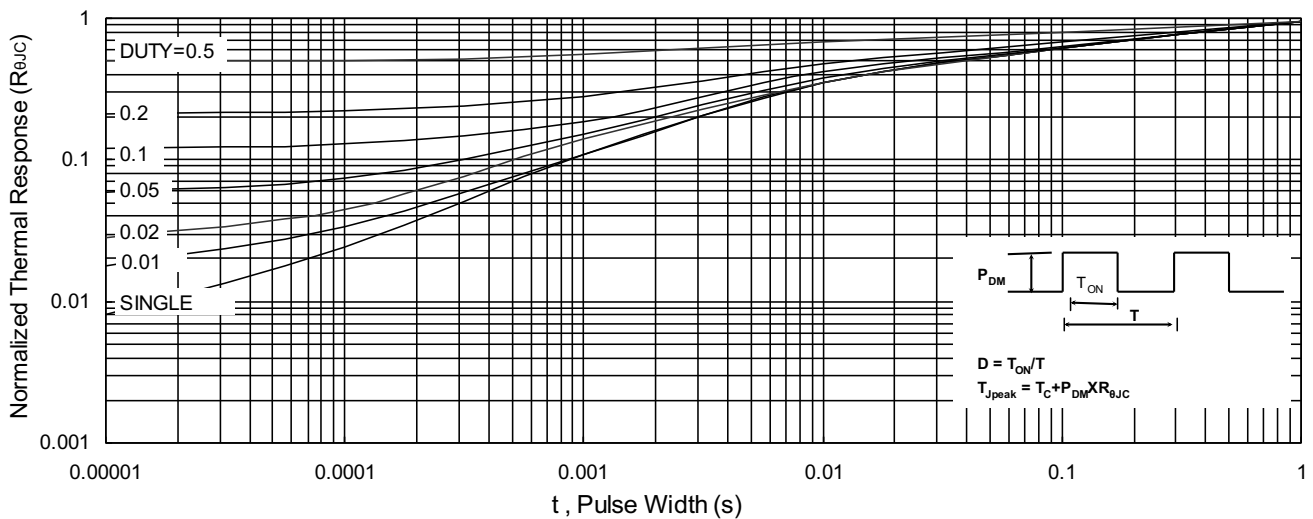


Fig.9 Normalized Maximum Transient Thermal Impedance

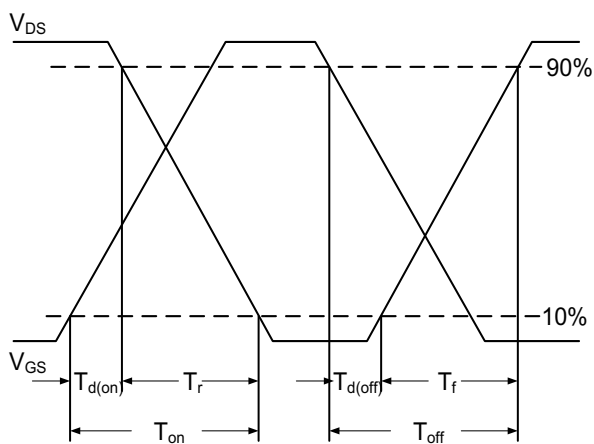


Fig.10 Switching Time Waveform

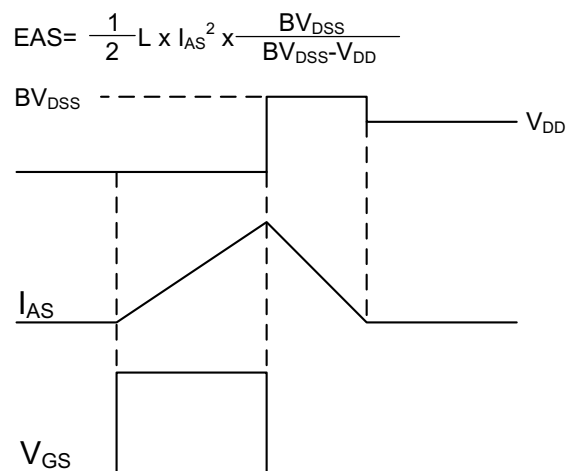


Fig.11 Unclamped Inductive Switching Waveform



Ordering Information

Part Number	Package code	Packaging
HSM0040	SOP-8	2500/Tape&Reel

