

Description

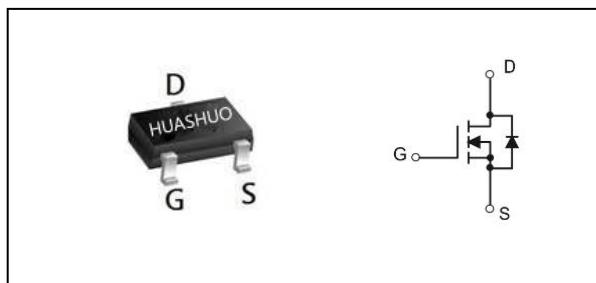
The HSST6072 is the high cell density trenched N-ch MOSFETs, which provides excellent RDSON and efficiency for most of the small power switching and load switch applications. The HSST6072 meets the RoHS and Green Product requirement with full function reliability approved.

- Voltage controlled small signal switch
- Rugged and reliable
- High saturation current capability
- Low Threshold

Product Summary

V _{DS}	60	V
R _{DS(ON),max}	5	Ω
I _D	120	mA

SOT523 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	60	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 4.5V ₁	120	mA
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 4.5V ₁	90	mA
P _D @T _A =25°C	Total Power Dissipation ³	0.15	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹	---	830	°C/W

Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	60	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ₂	$V_{\text{GS}}=10\text{V}$, $I_D=500\text{mA}$	---	---	5	Ω
		$V_{\text{GS}}=5\text{V}$, $I_D=50\text{mA}$	---	---	7	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	1	---	2.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^{\circ}\text{C}$	---	---	80	nA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 80	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_D=200\text{mA}$	---	80	---	mS
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=25\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_L=50\Omega$	---	20	---	ns
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	40	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	50	---	pF
C_{oss}	Output Capacitance		---	25	---	
C_{rss}	Reverse Transfer Capacitance		---	5	---	

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

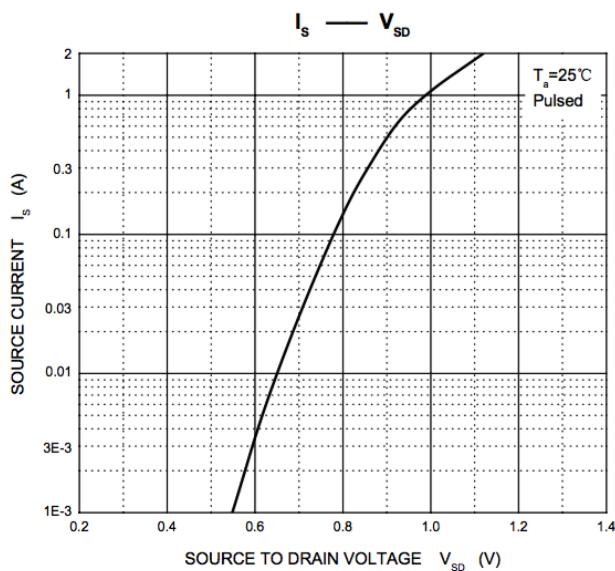
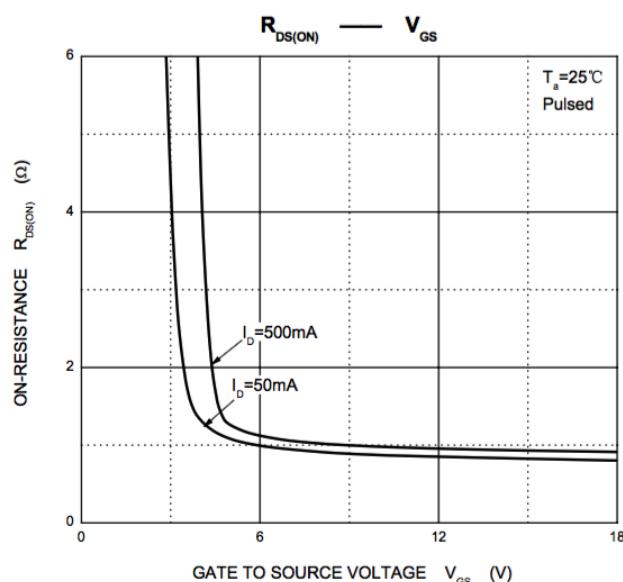
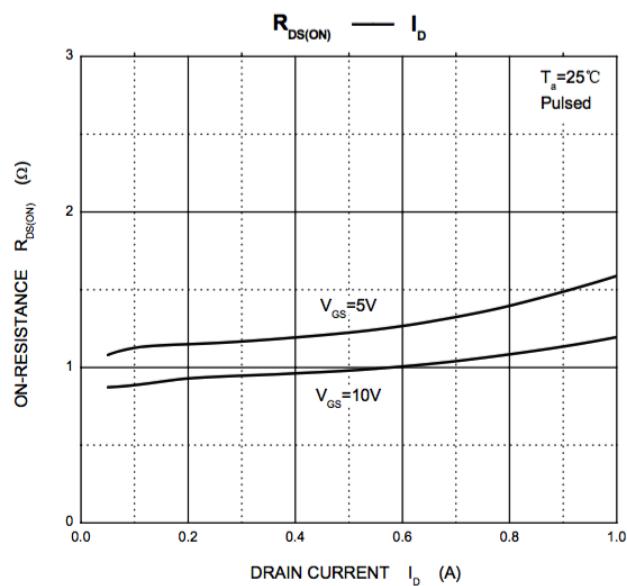
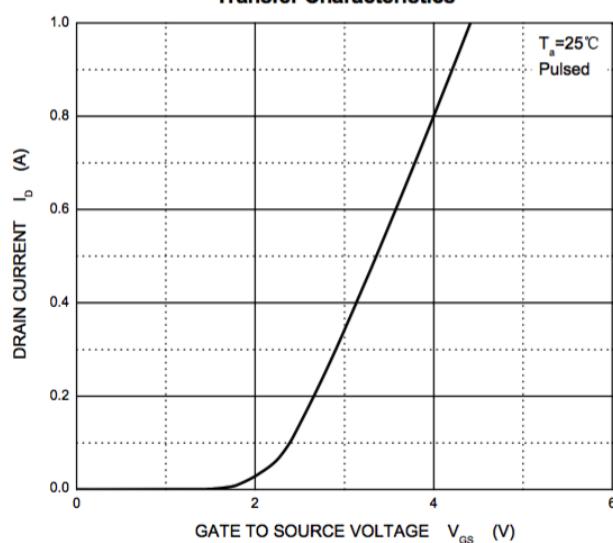


HUASHUO
SEMICONDUCTOR

HSST6072

N-Ch 60V Fast Switching MOSFETs

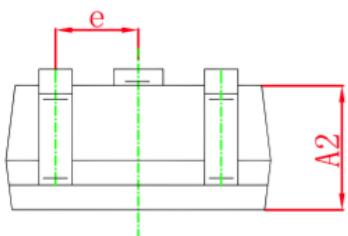
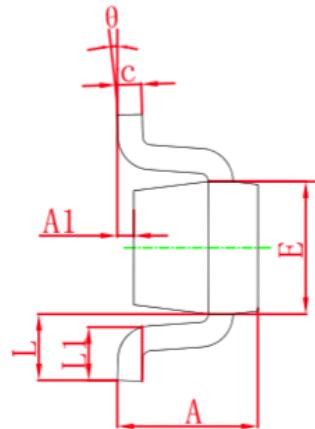
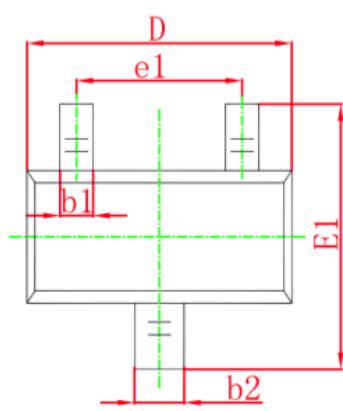
Transfer Characteristics





Ordering Information

Part Number	Package code	Packaging
HSST6072	SOT-523	3000/Tape&Reel



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.900	0.028	0.035
A1	0.000	0.100	0.000	0.004
A2	0.700	0.800	0.028	0.031
b1	0.150	0.250	0.006	0.010
b2	0.250	0.350	0.010	0.014
c	0.100	0.200	0.004	0.008
D	1.500	1.700	0.059	0.067
E	0.700	0.900	0.028	0.035
E1	1.450	1.750	0.057	0.069
e	0.500 TYP.		0.020 TYP.	
e1	0.900	1.100	0.035	0.043
L	0.400 REF.		0.016 REF.	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°