

### Description

The HSBB1R7N04 is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

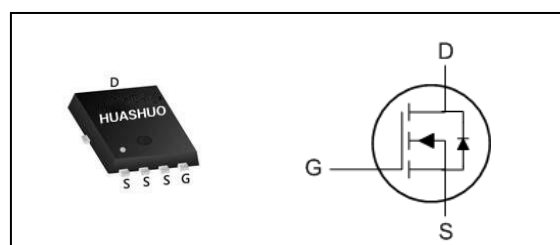
The HSBB1R7N04 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

- 100% EAS Guaranteed
- Green Device Available
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

### Product Summary

|                  |     |            |
|------------------|-----|------------|
| $V_{DS}$         | 40  | V          |
| $R_{DS(ON),typ}$ | 1.2 | m $\Omega$ |
| $I_D$            | 90  | A          |

### PRPAK3\*3 Pin Configuration



### Absolute Maximum Ratings

| Symbol                | Parameter   | Rating     | Units      |
|-----------------------|---|------------|------------|
| $V_{DS}$              | Drain-Source Voltage                                  | 40         | V          |
| $V_{GS}$              | Gate-Source Voltage                                   | $\pm 20$   | V          |
| $I_D@T_C=25^\circ C$  | Continuous Drain Current, $V_{GS}$ @ 10V <sup>1</sup> | 90         | A          |
| $I_D@T_C=100^\circ C$ | Continuous Drain Current, $V_{GS}$ @ 10V <sup>1</sup> | 61         | A          |
| $I_{DM}$              | Pulsed Drain Current <sup>2</sup>                     | 240        | A          |
| EAS                   | Single Pulse Avalanche Energy <sup>3</sup>            | 338        | mJ         |
| $I_S@T_C=25^\circ C$  | Avalanche Current                                     | 90         | A          |
| $P_D@T_C=25^\circ C$  | Total Power Dissipation <sup>4</sup>                  | 35         | W          |
| $T_{STG}$             | Storage Temperature Range                             | -55 to 150 | $^\circ C$ |
| $T_J$                 | Operating Junction Temperature Range                  | -55 to 150 | $^\circ C$ |

### Thermal Data

| Symbol          | Parameter  | Typ. | Max. | Unit         |
|-----------------|--|------|------|--------------|
| $R_{\theta JA}$ | Thermal Resistance Junction-Ambient <sup>1</sup> | ---  | 62.5 | $^\circ C/W$ |
| $R_{\theta JC}$ | Thermal Resistance Junction-Case <sup>1</sup>    | ---  | 3.5  | $^\circ C/W$ |

**Electrical Characteristics ( $T_J=25\text{ }^\circ\text{C}$ , unless otherwise noted)**

| Symbol       | Parameter                                      | Conditions   | Min. | Typ. | Max.      | Unit      |
|--------------|--|--|------|------|-----------|-----------|
| $BV_{DSS}$   | Drain-Source Breakdown Voltage                 | $V_{GS}=0V, I_D=250\mu A$  | 40   | ---  | ---       | V         |
| $R_{DS(ON)}$ | Static Drain-Source On-Resistance <sup>2</sup> | $V_{GS}=10V, I_D=30A$  | ---  | 1.2  | 1.7       | $m\Omega$ |
|              |  | $V_{GS}=4.5V, I_D=20A$   | ---  | 2.3  | 2.8       |           |
| $V_{GS(th)}$ | Gate Threshold Voltage                         | $V_{GS}=V_{DS}, I_D=250\mu A$                                    | 1.0  | ---  | 3.0       | V         |
| $I_{DSS}$    | Drain-Source Leakage Current                   | $V_{DS}=32V, V_{GS}=0V, T_J=25^\circ C$                          | ---  | ---  | 1         | $\mu A$   |
|              |  | $V_{DS}=32V, V_{GS}=0V, T_J=55^\circ C$                          | ---  | ---  | 5         |           |
| $I_{GSS}$    | Gate-Source Leakage Current                    | $V_{GS}=\pm 20V, V_{DS}=0V$                                      | ---  | ---  | $\pm 100$ | nA        |
| $Q_g$        | Total Gate Charge                              | $V_{DS}=20V, V_{GS}=10V, I_D=30A$                                | ---  | 51   | ---       | nC        |
| $Q_{gs}$     | Gate-Source Charge                             |  | ---  | 11   | ---       |           |
| $Q_{gd}$     | Gate-Drain Charge                              |  | ---  | 9    | ---       |           |
| $T_{d(on)}$  | Turn-On Delay Time                             | $V_{DD}=20V, V_{GS}=10V, R_G=3.9\Omega, R_L=0.66\Omega, I_D=30A$ | ---  | 10   | ---       | ns        |
| $T_r$        | Rise Time                                      |  | ---  | 68   | ---       |           |
| $T_{d(off)}$ | Turn-Off Delay Time                            |  | ---  | 49   | ---       |           |
| $T_f$        | Fall Time                                      |  | ---  | 40   | ---       |           |
| $C_{iss}$    | Input Capacitance                              | $V_{DS}=20V, V_{GS}=0V, f=1MHz$                                  | ---  | 2684 | ---       | pF        |
| $C_{oss}$    | Output Capacitance                             |  | ---  | 1025 | ---       |           |
| $C_{riss}$   | Reverse Transfer Capacitance                   |  | ---  | 75   | ---       |           |

**Diode Characteristics**

| Symbol   | Parameter                                | Conditions                                  | Min. | Typ. | Max. | Unit |
|----------|--|---|------|------|------|------|
| $I_S$    | Continuous Source Current <sup>1,5</sup> | $V_G=V_D=0V$ , Force Current                | ---  | ---  | 90   | A    |
| $V_{SD}$ | Diode Forward Voltage <sup>2</sup>       | $V_{GS}=0V, I_S=A, T_J=25^\circ C$          | ---  | ---  | 1.3  | V    |
| $t_{rr}$ | Reverse Recovery Time                    | $I_F=30A, dI/dt=100A/\mu s, T_J=25^\circ C$ | ---  | 47   | ---  | nS   |
| $Q_{rr}$ | Reverse Recovery Charge                  |   | ---  | 35   | ---  | nC   |

Note :

 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper.

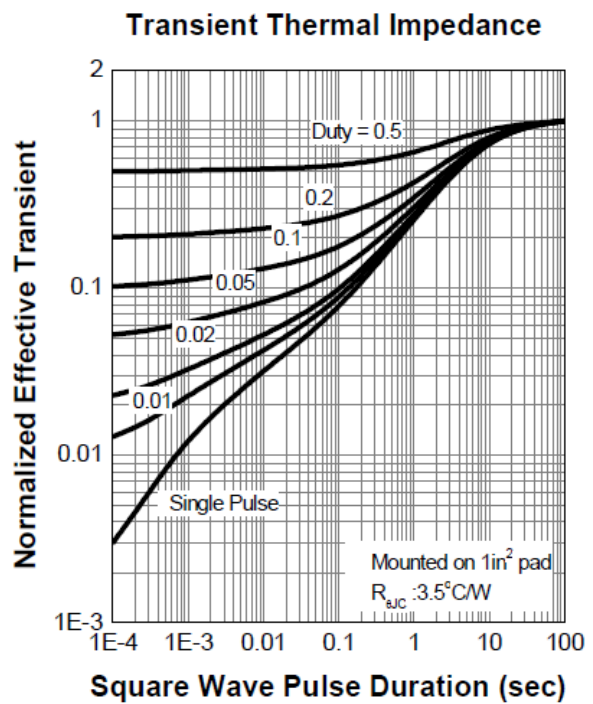
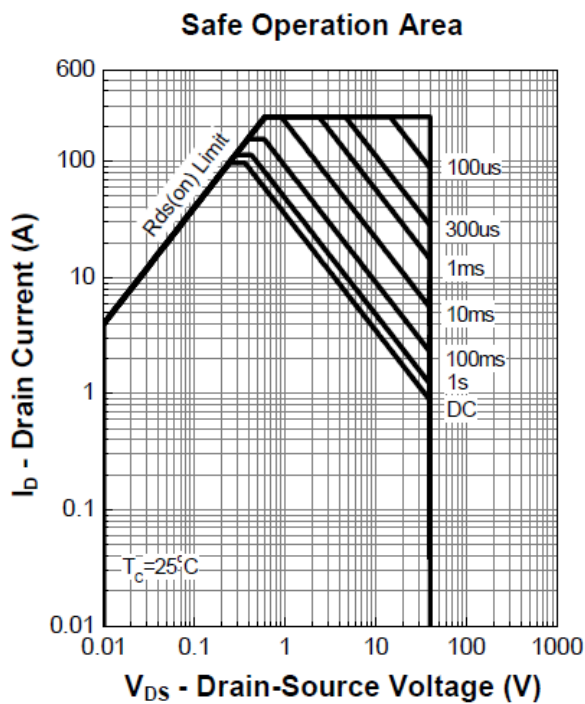
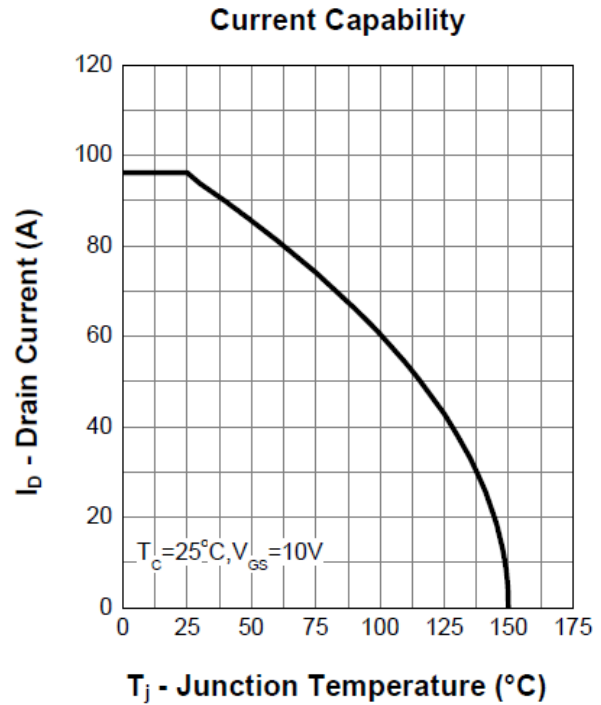
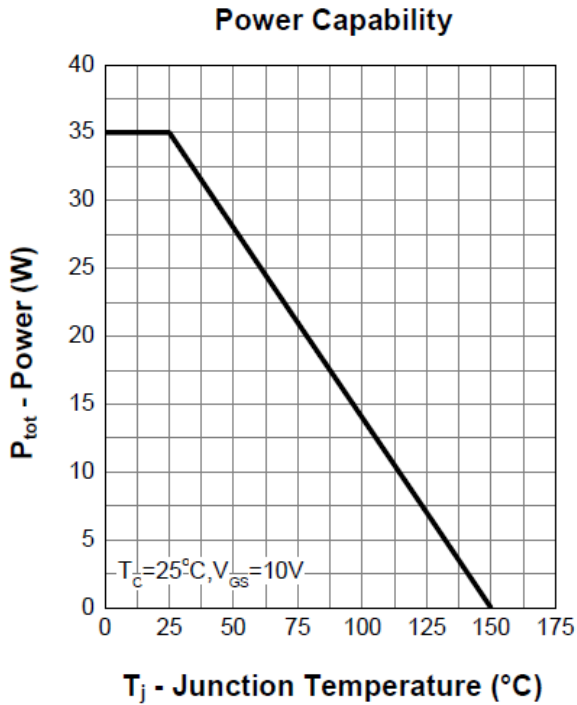
 2.The data tested by pulsed , pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ 

 3.The power dissipation is limited by 150 $^\circ C$  junction temperature

 4.The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications , should be limited by total power dissipation.

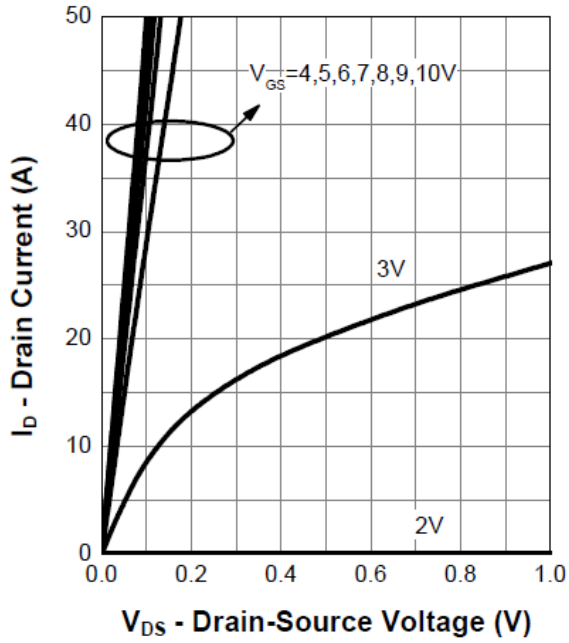


**Typical Characteristics**

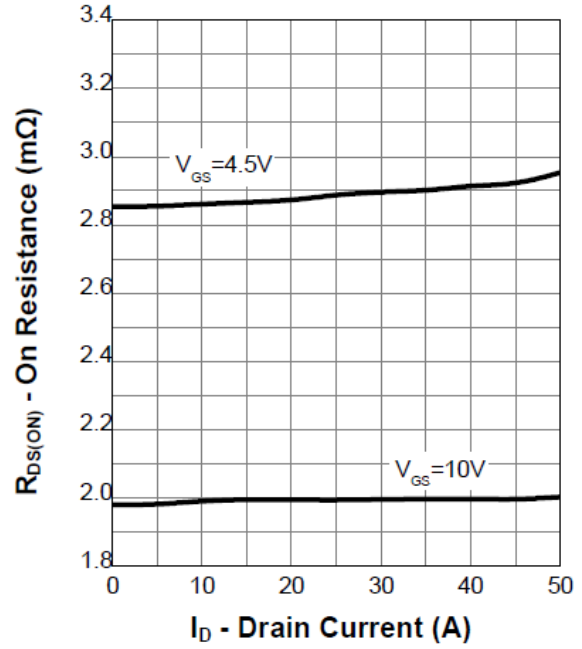




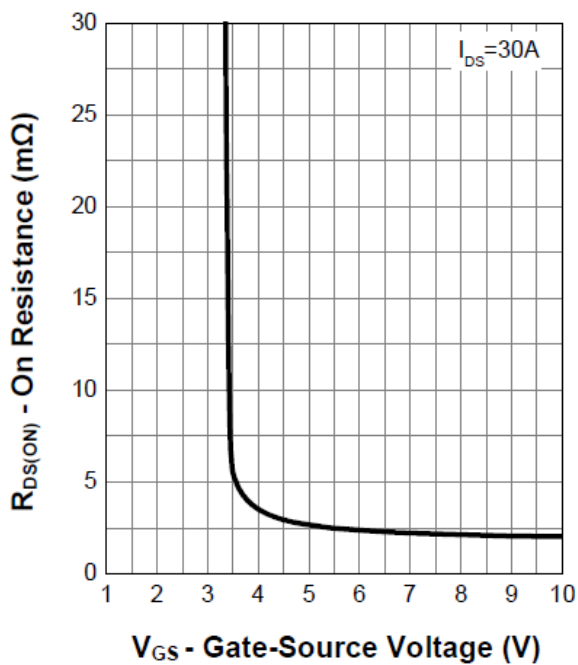
### Output Characteristics



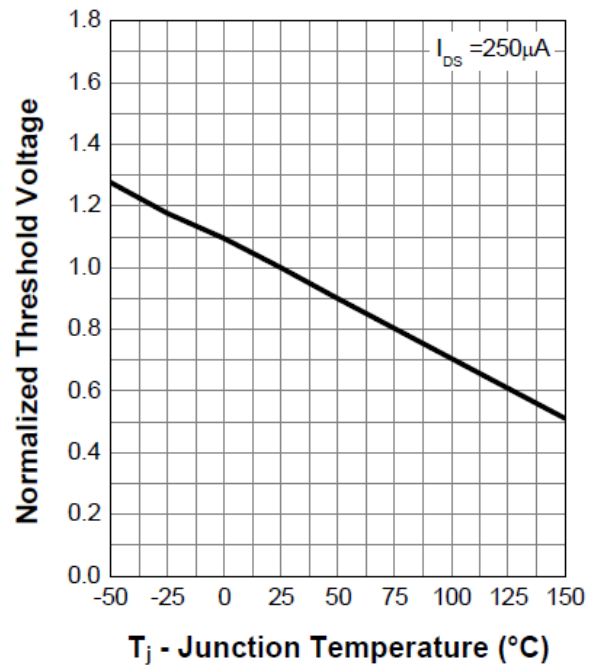
### On Resistance

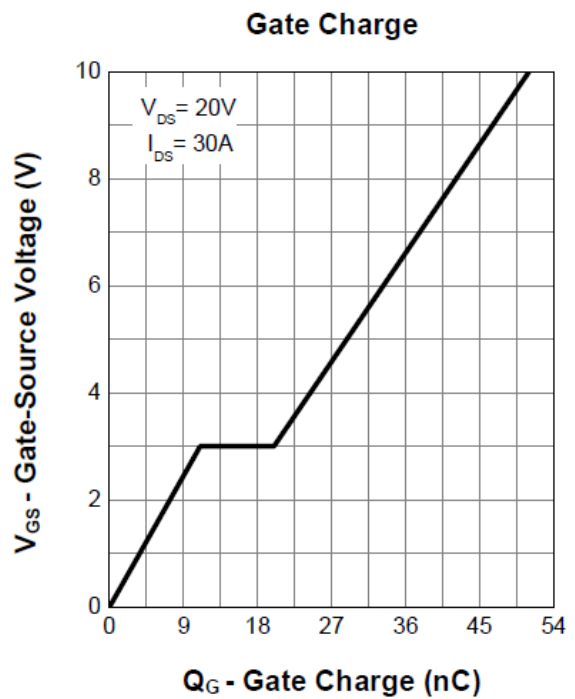
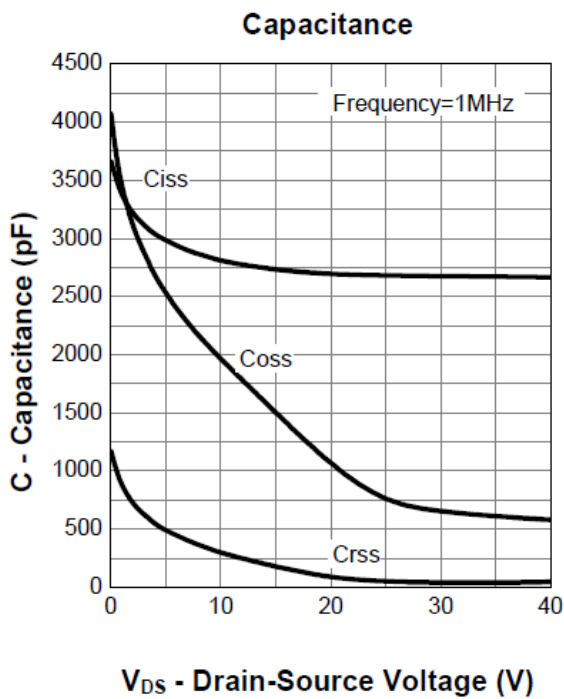
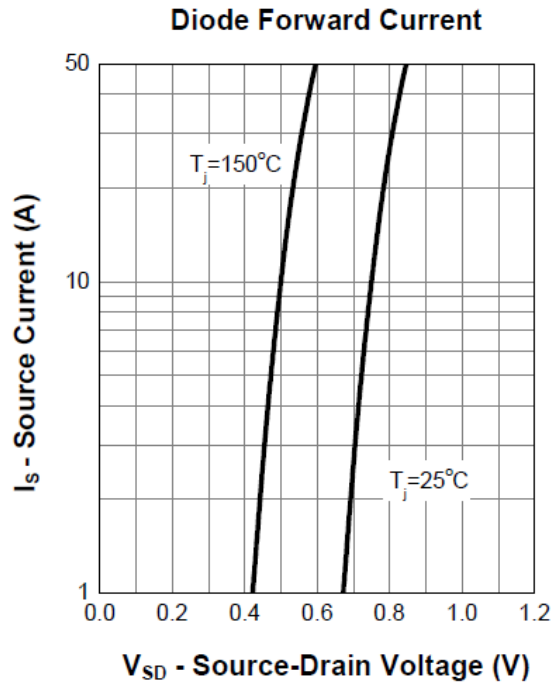
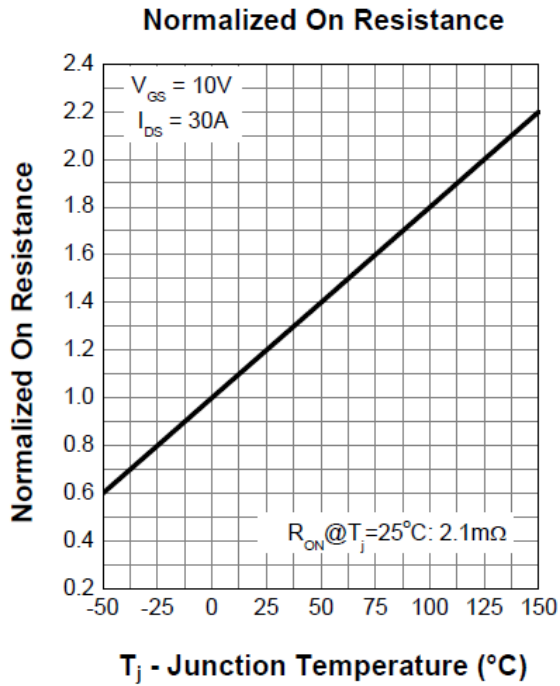


### Transfer Characteristics



### Normalized Threshold Voltage







**PDFN 3x3-8L Package**

