

Description

The HSH0016 is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

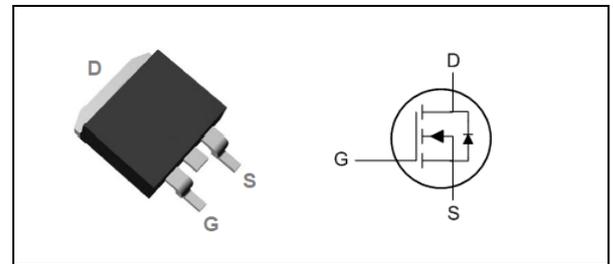
The HSH0016 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available
- Advanced high cell density Trench technology

Product Summary

V_{DS}	100	V
$R_{DS(ON),max}$	40	m Ω
I_D	36	A

TO263 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	36	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	26	A
I_{DM}	Pulsed Drain Current ²	72	A
EAS	Single Pulse Avalanche Energy ³	126	mJ
I_{AS}	Avalanche Current	13	A
$P_D@T_C=25^\circ C$	Total Power Dissipation ⁴	125	W
T_{STG}	Storage Temperature Range	-55 to 175	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 175	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	1.2	$^\circ C/W$



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	100	---	---	V
ΔBV _{DSS} /ΔT _J	BVDSS Temperature Coefficient	Reference to 25°C, I _D =1mA	---	0.098	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =20A	---	---	40	mΩ
		V _{GS} =4.5V, I _D =15A	---	---	50	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.5	---	2.5	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-5.52	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =80V, V _{GS} =0V, T _J =25°C	---	---	10	uA
		V _{DS} =80V, V _{GS} =0V, T _J =55°C	---	---	100	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =5V, I _D =20A	---	30	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	1.6	---	Ω
Q _g	Total Gate Charge (10V)	V _{DS} =80V, V _{GS} =10V, I _D =20A	---	46	---	nC
Q _{gs}	Gate-Source Charge		---	6.7	---	
Q _{gd}	Gate-Drain Charge		---	11.8	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} =50V, V _{GS} =10V, R _G =3.3Ω I _D =20A	---	10.4	---	ns
T _r	Rise Time		---	32	---	
T _{d(off)}	Turn-Off Delay Time		---	42	---	
T _f	Fall Time		---	14	---	
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz	---	2270	---	pF
C _{oss}	Output Capacitance		---	137	---	
C _{rss}	Reverse Transfer Capacitance		---	82	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V, Force Current	---	---	36	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1.2	V
t _{rr}	Reverse Recovery Time	I _F =20A, dI/dt=100A/μs, T _J =25°C	---	33	---	nS
Q _{rr}	Reverse Recovery Charge		---	28	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The EAS data shows Max. rating. The test condition is V_{DD}=25V, V_{GS}=10V, L=1.5mH, I_{AS}=13A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.



Typical Characteristics

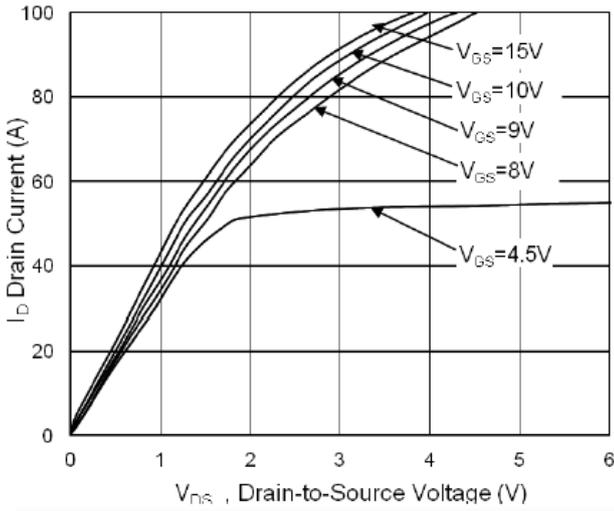


Fig.1 Typical Output Characteristics

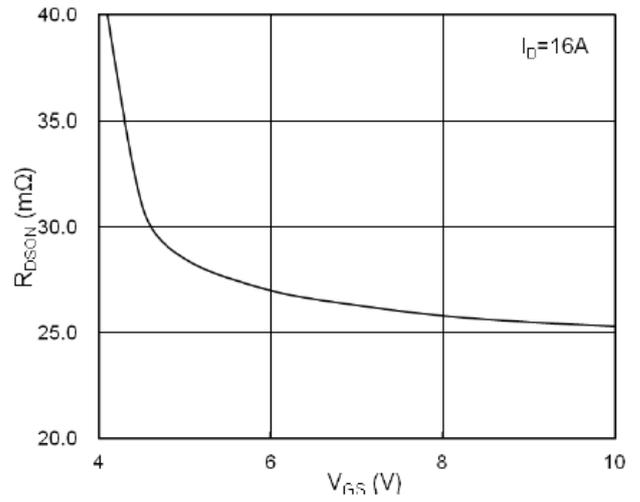


Fig.2 On-Resistance vs. Gate-Source

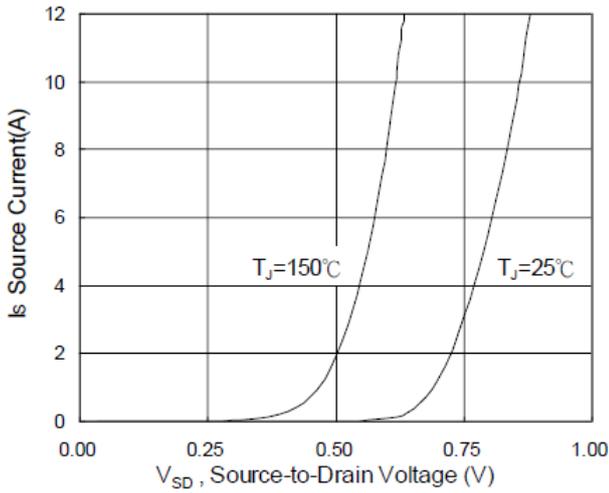


Fig.3 Forward Characteristics Of Reverse

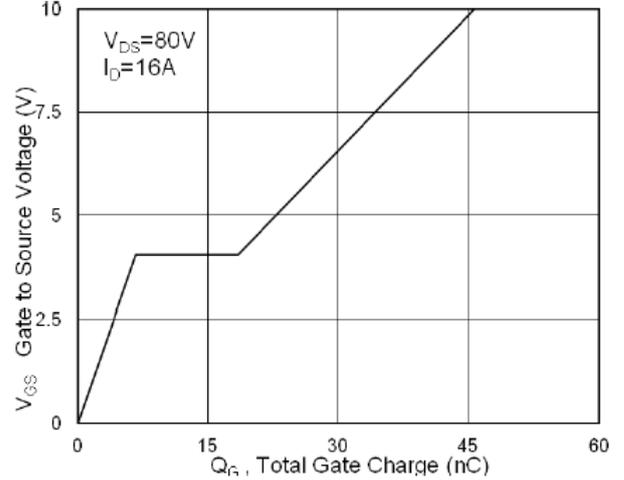


Fig.4 Gate-Charge Characteristics

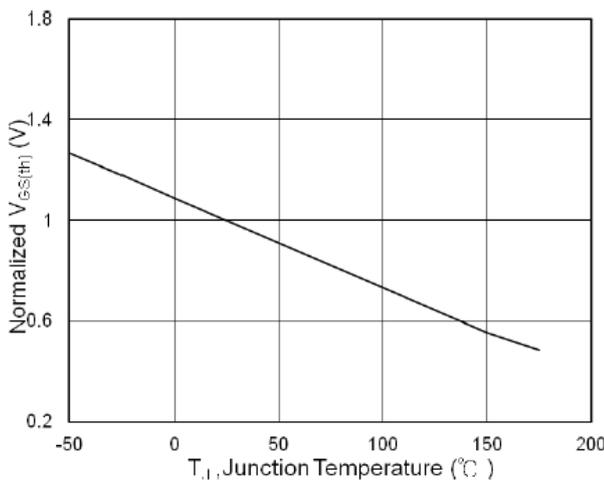


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

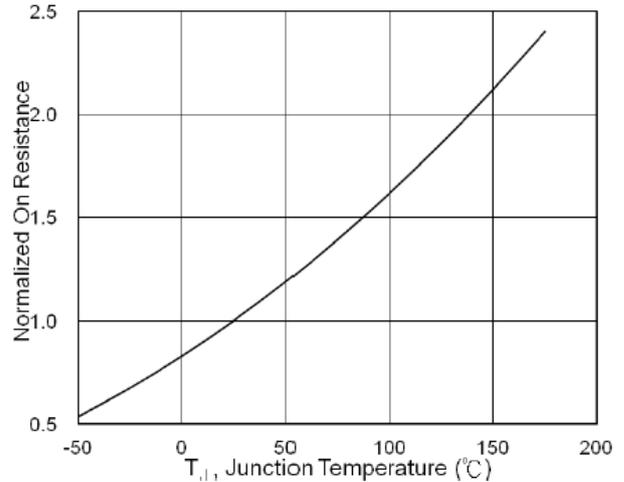


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

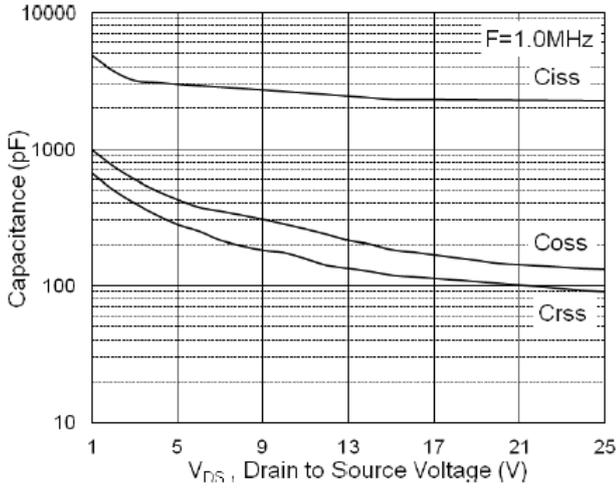


Fig.7 Capacitance

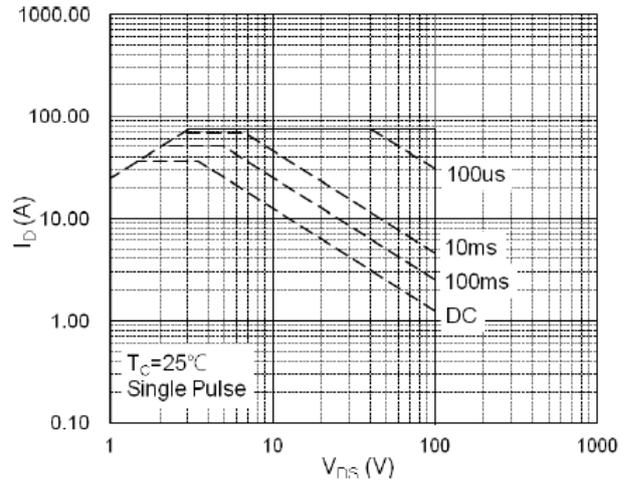


Fig.8 Safe Operating Area

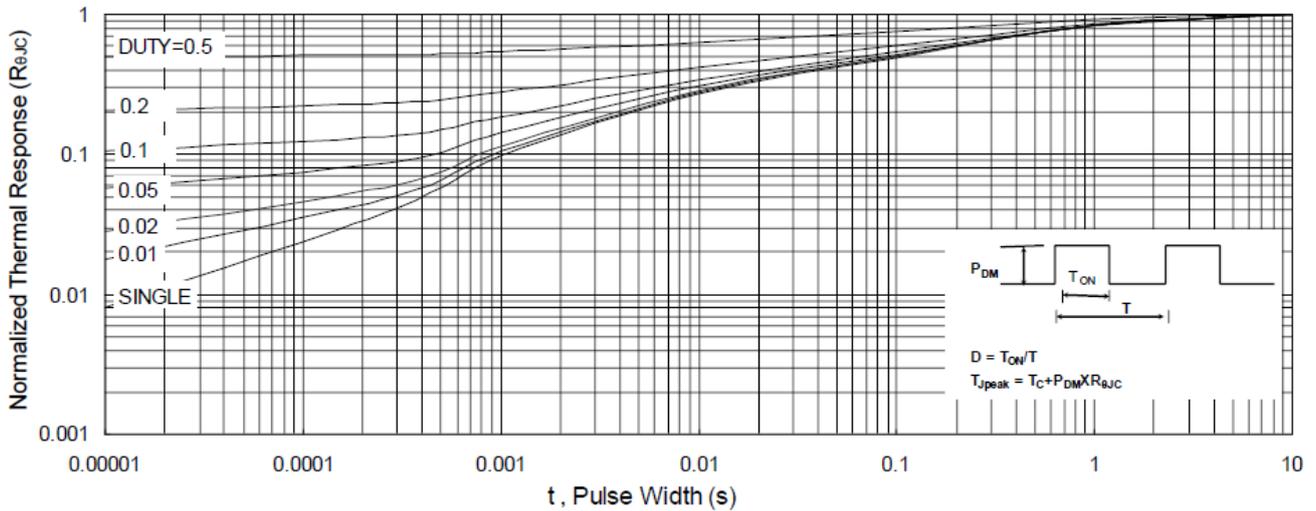


Fig.9 Normalized Maximum Transient Thermal Impedance

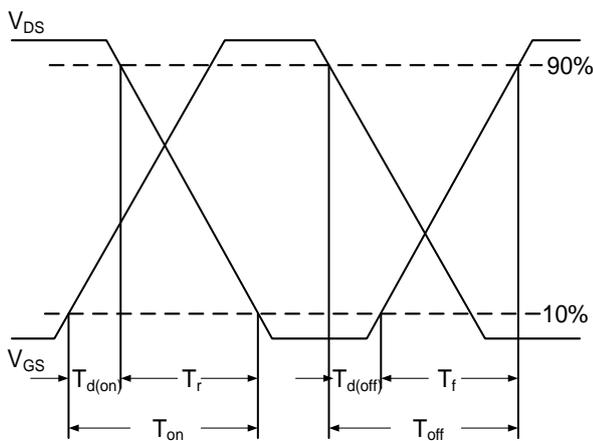


Fig.10 Switching Time Waveform

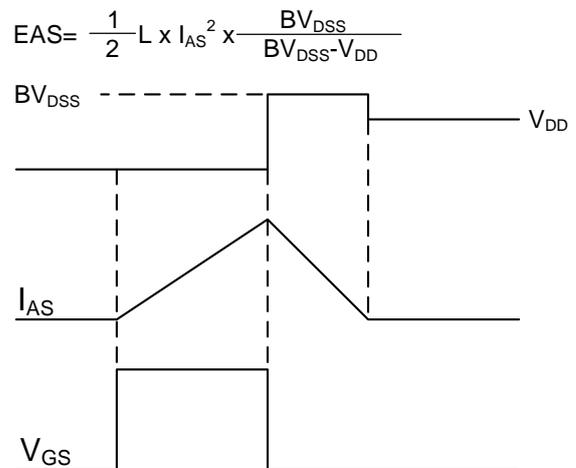
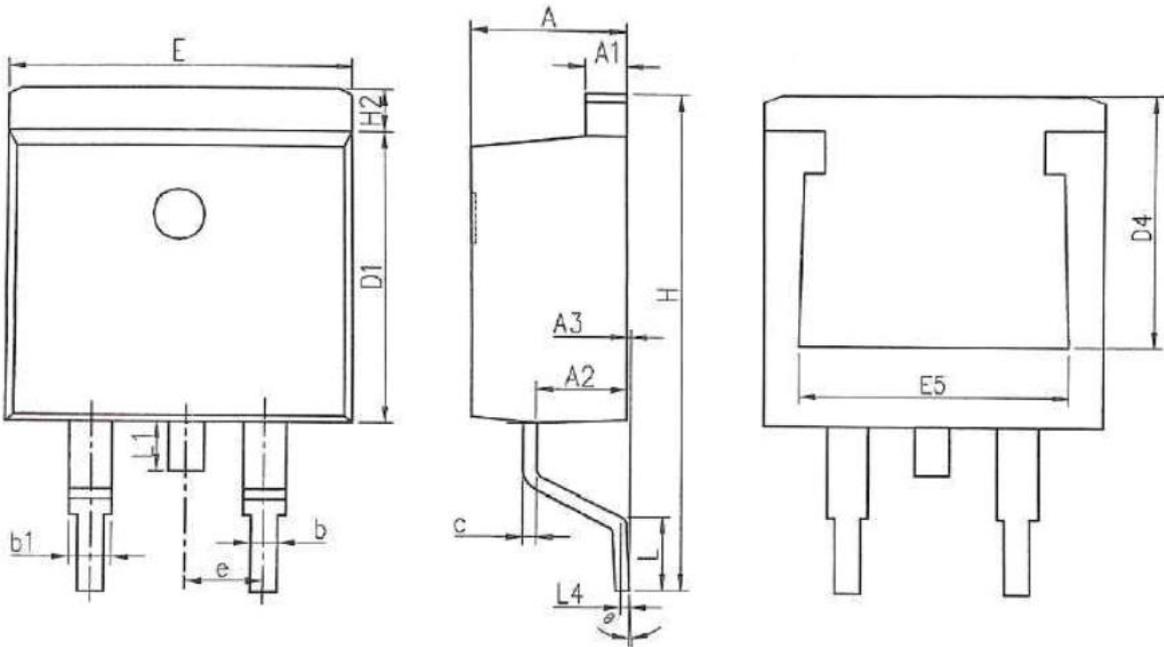


Fig.11 Unclamped Inductive Switching

$$EAS = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.370	4.770	0.172	0.188
A1	1.220	1.420	0.048	0.056
A2	2.200	2.890	0.087	0.114
A3	0.000	0.250	0.000	0.010
b	0.700	0.960	0.028	0.038
b1	1.170	1.470	0.046	0.058
c	0.300	0.530	0.012	0.021
D1	8.500	9.300	0.335	0.366
D4	6.600	-	0.260	-
E	9.860	10.36	0.388	0.408
E5	7.060	-	0.278	-
e	2.540 BSC		0.100 BSC	
H	14.70	15.70	0.579	0.618
H2	1.070	1.470	0.042	0.058
L	2.000	2.600	0.079	0.102
L1	1.400	1.750	0.055	0.069
L4	0.250 BSC		0.010 BSC	
θ	0°	9°	0°	9°