

### Description

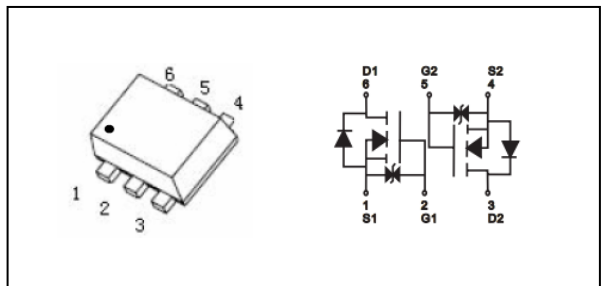
The HSSX2204 is the high cell density trench N-ch MOSFETs, which provides excellent RDSON and efficiency for most of the small power switching and load switch applications. The HSSX2204 meets the RoHS and Green Product requirement with full function reliability approved.

- Fast Switching Speed
- Super Low Gate Charge
- High-Side Switching
- Low Threshold
- ESD Protected up to 2KV

### Product Summary

|                  |     |            |
|------------------|-----|------------|
| $V_{DS}$         | 20  | V          |
| $R_{DS(ON),Max}$ | 500 | m $\Omega$ |
| $I_D$            | 0.6 | A          |

### SOT-563 Pin Configuration



### Absolute Maximum Ratings

| Symbol                     | Parameter                                   | Rating     | Units            |
|----------------------------|---------------------------------------------|------------|------------------|
| $V_{DS}$                   | Drain-Source Voltage                        | 20         | V                |
| $V_{GS}$                   | Gate-Source Voltage                         | $\pm 12$   | V                |
| $I_D@T_A=25^\circ\text{C}$ | Continuous Drain Current, $V_{GS} @ 4.5V^1$ | 0.8        | A                |
| $I_{DM}$                   | Pulsed Drain Current <sup>2</sup>           | 3.2        | A                |
| $P_D@T_A=25^\circ\text{C}$ | Total Power Dissipation <sup>3</sup>        | 0.15       | W                |
| $T_{STG}$                  | Storage Temperature Range                   | -55 to 150 | $^\circ\text{C}$ |
| $T_J$                      | Operating Junction Temperature Range        | -55 to 150 | $^\circ\text{C}$ |

### Thermal Data

| Symbol          | Parameter                                        | Typ. | Max. | Unit               |
|-----------------|--------------------------------------------------|------|------|--------------------|
| $R_{\theta JA}$ | Thermal Resistance Junction-ambient <sup>1</sup> | ---  | 800  | $^\circ\text{C/W}$ |



### Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

| Symbol       | Parameter                                      | Conditions                                                | Min. | Typ. | Max.     | Unit      |
|--------------|------------------------------------------------|-----------------------------------------------------------|------|------|----------|-----------|
| $BV_{DSS}$   | Drain-Source Breakdown Voltage                 | $V_{GS}=0V$ , $I_D=250\mu A$                              | 20   | ---  | ---      | V         |
| $R_{DS(on)}$ | Static Drain-Source On-Resistance <sup>2</sup> | $V_{GS}=4.5V$ , $I_D=600mA$                               | ---  | 300  | 500      | $m\Omega$ |
|              |                                                | $V_{GS}=2.5V$ , $I_D=400mA$                               | ---  | 600  | 800      |           |
| $V_{GS(th)}$ | Gate Threshold Voltage                         | $V_{GS}=V_{DS}$ , $I_D=250\mu A$                          | 0.5  | 0.7  | 1.0      | V         |
| $I_{DSS}$    | Drain-Source Leakage Current                   | $V_{DS}=20V$ , $V_{GS}=0V$ , $T_J=25^\circ\text{C}$       | ---  | ---  | 1        | $\mu A$   |
|              |                                                | $V_{DS}=20V$ , $V_{GS}=0V$ , $T_J=55^\circ\text{C}$       | ---  | ---  | 5        |           |
| $I_{GSS}$    | Gate-Source Leakage Current                    | $V_{GS}=\pm 8V$ , $V_{DS}=0V$                             | ---  | ---  | $\pm 10$ | $\mu A$   |
| $Q_g$        | Total Gate Charge (4.5V)                       | $V_{DS}=6V$ , $V_{GS}=4.5V$ , $I_D=0.3A$                  | ---  | 3.6  | ---      | $nC$      |
| $Q_{gs}$     | Gate-Source Charge                             |                                                           | ---  | 2.1  | ---      |           |
| $Q_{gd}$     | Gate-Drain Charge                              |                                                           | ---  | 1.5  | ---      |           |
| $T_{d(on)}$  | Turn-On Delay Time                             | $V_{DD}=6V$ , $V_{GS}=4.5V$ , $R_G=6\Omega$<br>$I_D=0.3A$ | ---  | 5.8  | ---      | $ns$      |
| $T_r$        | Rise Time                                      |                                                           | ---  | 15   | ---      |           |
| $T_{d(off)}$ | Turn-Off Delay Time                            |                                                           | ---  | 21   | ---      |           |
| $T_f$        | Fall Time                                      |                                                           | ---  | 7.1  | ---      |           |
| $C_{iss}$    | Input Capacitance                              | $V_{DS}=6V$ , $V_{GS}=0V$ , $f=1MHz$                      | ---  | 42   | ---      | $pF$      |
| $C_{oss}$    | Output Capacitance                             |                                                           | ---  | 25   | ---      |           |
| $C_{rss}$    | Reverse Transfer Capacitance                   |                                                           | ---  | 12   | ---      |           |

### Diode Characteristics

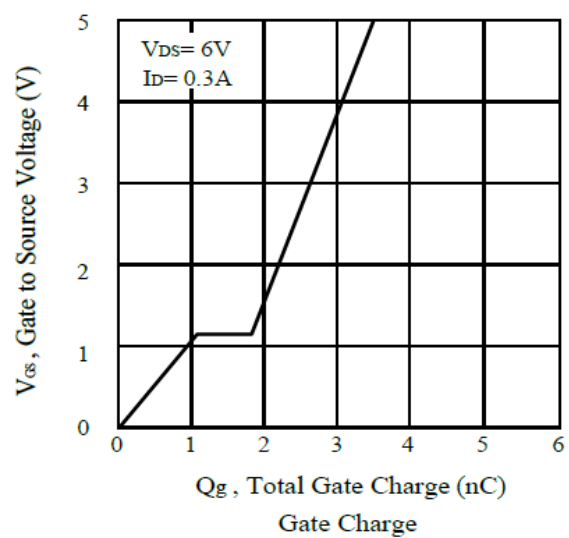
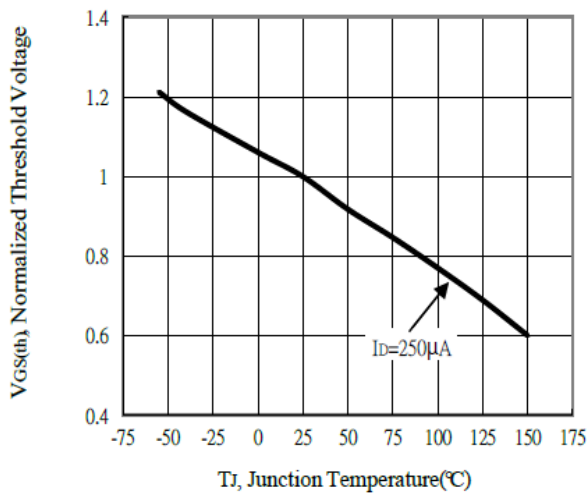
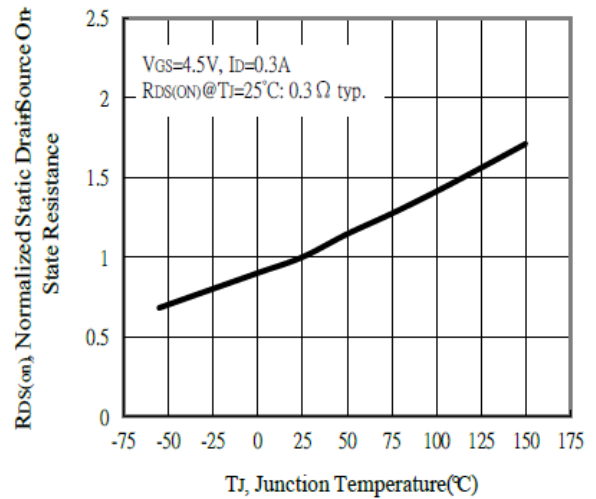
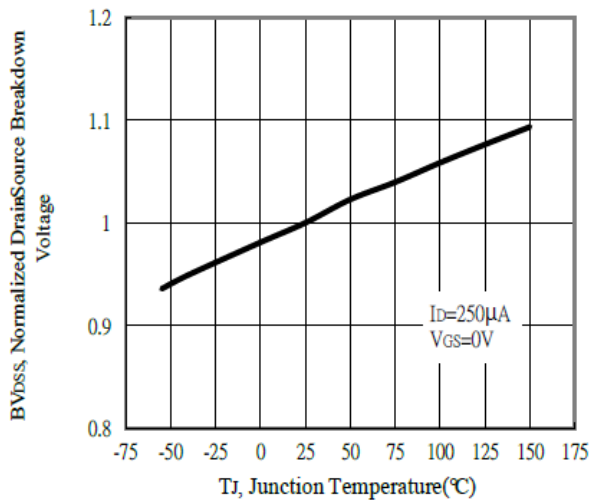
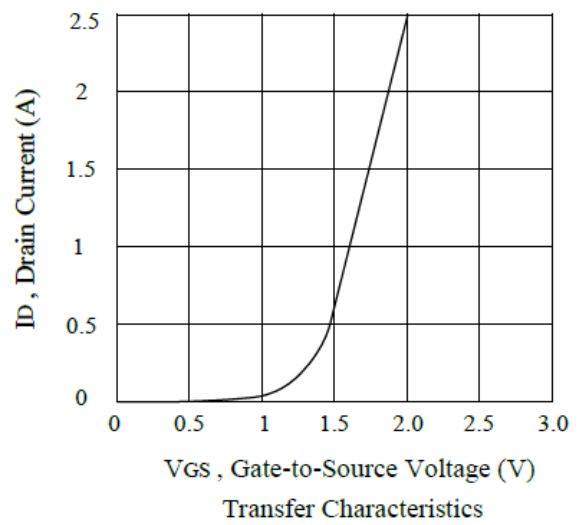
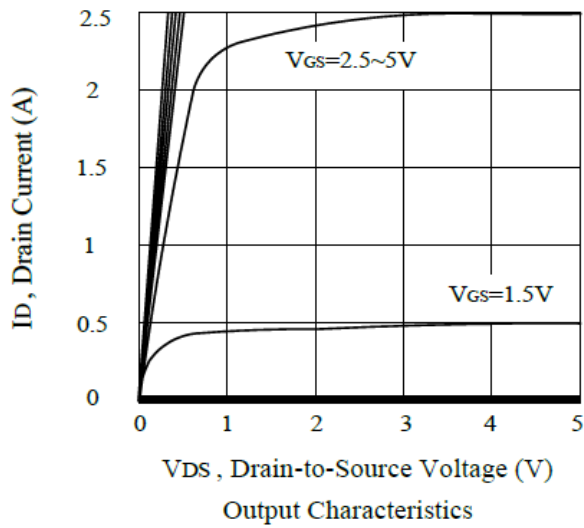
| Symbol   | Parameter                                | Conditions                                        | Min. | Typ. | Max. | Unit |
|----------|------------------------------------------|---------------------------------------------------|------|------|------|------|
| $I_S$    | Continuous Source Current <sup>1,4</sup> | $V_G=V_D=0V$ , Force Current                      | ---  | ---  | 0.8  | A    |
| $V_{SD}$ | Diode Forward Voltage <sup>2</sup>       | $V_{GS}=0V$ , $I_S=0.3A$ , $T_J=25^\circ\text{C}$ | ---  | ---  | 1.2  | V    |

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$
- 3.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 4.The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

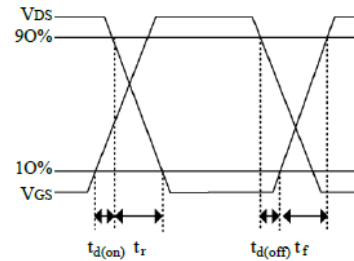
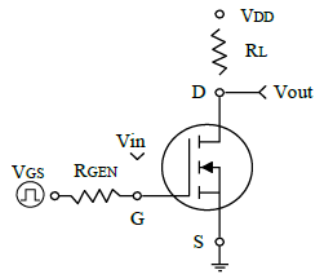
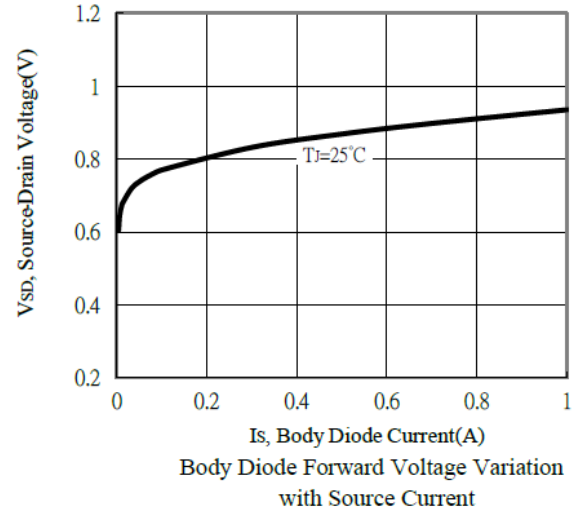
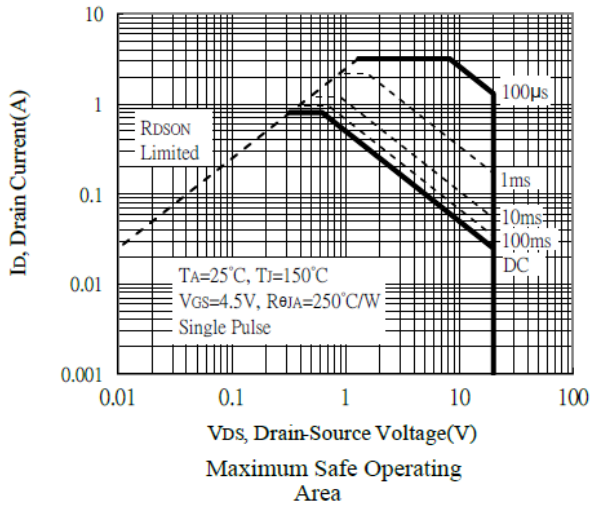


### Typical Characteristics

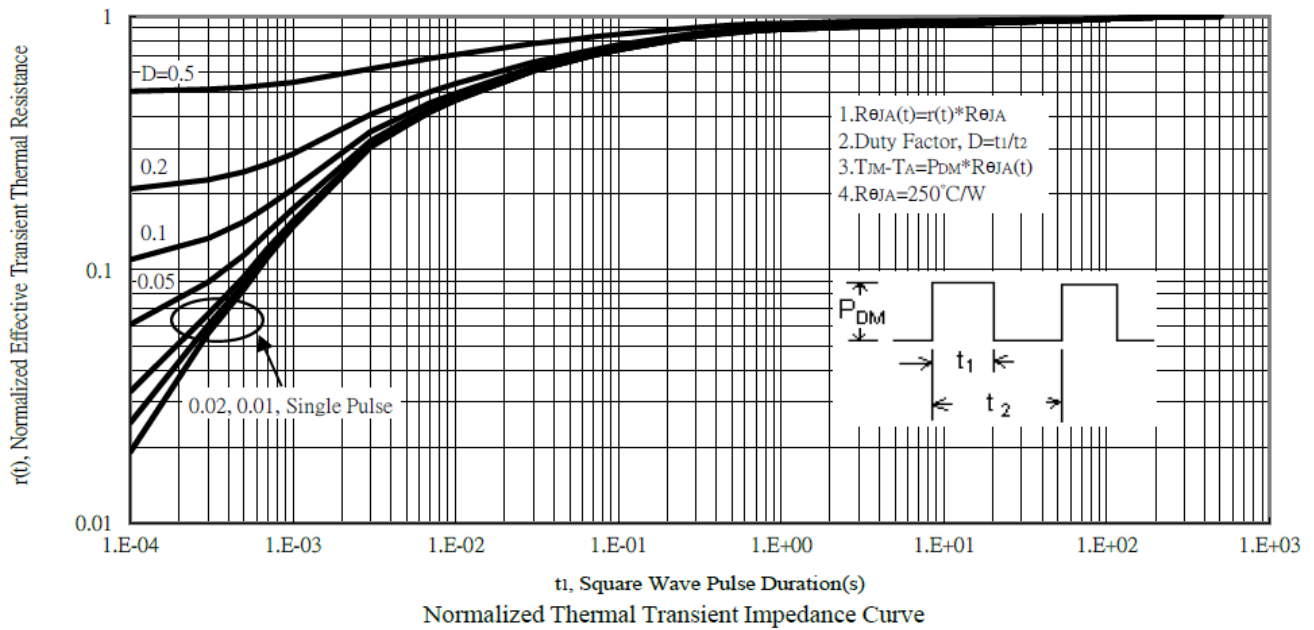




## Dual N 20V Fast Switching MOSFETs



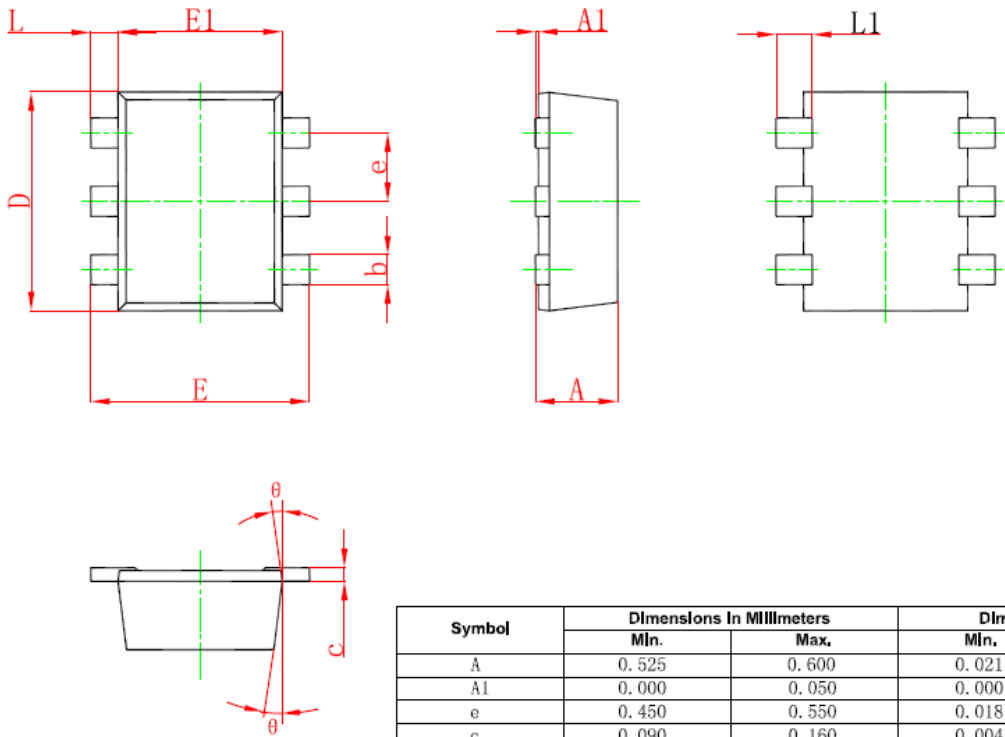
Switching Test Circuit and Switching Waveforms





Ordering Information

| Part Number | Package code | Packaging      |
|-------------|--------------|----------------|
| HSSX2204    | SOT-563      | 3000/Tape&Reel |



| Symbol | Dimensions In Millimeters |       | Dimensions In Inches |       |
|--------|---------------------------|-------|----------------------|-------|
|        | Min.                      | Max.  | Min.                 | Max.  |
| A      | 0.525                     | 0.600 | 0.021                | 0.024 |
| A1     | 0.000                     | 0.050 | 0.000                | 0.002 |
| e      | 0.450                     | 0.550 | 0.018                | 0.022 |
| c      | 0.090                     | 0.160 | 0.004                | 0.006 |
| D      | 1.500                     | 1.700 | 0.059                | 0.067 |
| b      | 0.170                     | 0.270 | 0.007                | 0.011 |
| E1     | 1.100                     | 1.300 | 0.043                | 0.051 |
| E      | 1.500                     | 1.700 | 0.059                | 0.067 |
| L      | 0.100                     | 0.300 | 0.004                | 0.012 |
| L1     | 0.200                     | 0.400 | 0.008                | 0.016 |
| θ      | 7 °REF.                   |       | 7 °REF.              |       |