

Description

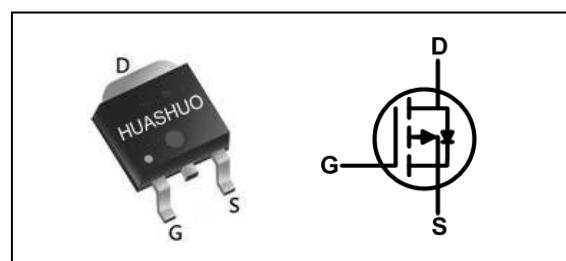
The HSU6117 is the high cell density trenched P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The HSU6117 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Product Summary

V _{DS}	-60	V
R _{DS(ON),max}	14	mΩ
I _D	-60	A

TO252 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-60	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, -V _{GS} @ -10V ¹	-60	A
I _D @T _C =100°C	Continuous Drain Current, -V _{GS} @ -10V ¹	-35	A
I _{DM}	Pulsed Drain Current ²	-170	A
EAS	Single Pulse Avalanche Energy ³	330	mJ
I _{AS}	Avalanche Current	40	A
P _D @T _C =25°C	Total Power Dissipation ⁴	90	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	62	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	1.4	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=-250\mu\text{A}$	-60	---	---	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=-10\text{V}$, $I_D=-18\text{A}$	---	---	14	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_D=-12\text{A}$	---	---	17	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = -250\mu\text{A}$	-1.0	---	-2.5	V
$\Delta V_{\text{GS}(\text{th})}$	$V_{\text{GS}(\text{th})}$ Temperature Coefficient		---	4.28	---	$\text{mV}/^\circ\text{C}$
I_{DSs}	Drain-Source Leakage Current	$V_{\text{DS}}=-48\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{\text{DS}}=-48\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$, $I_D=-18\text{A}$	---	43	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	2.6	---	Ω
Q_g	Total Gate Charge	$V_{\text{DS}}=-30\text{V}$, $V_{\text{GS}}=-10\text{V}$, $I_D=-12\text{A}$	---	85	---	nC
Q_{gs}	Gate-Source Charge		---	11	---	
Q_{gd}	Gate-Drain Charge		---	30	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=-30\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_g=6\Omega$, $I_D=-1\text{A}$	---	18	---	ns
T_r	Rise Time		---	12	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	100	---	
T_f	Fall Time		---	68	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-30\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	4635	---	pF
C_{oss}	Output Capacitance		---	524	---	
C_{rss}	Reverse Transfer Capacitance		---	241	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,5}	$V_G=V_D=0\text{V}$, Force Current	---	---	-40	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $I_s=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=-30\text{V}$, $V_{\text{GS}}=-10\text{V}$, $L=0.5\text{mH}$, $I_{\text{AS}}=-40\text{A}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.



Typical Characteristics

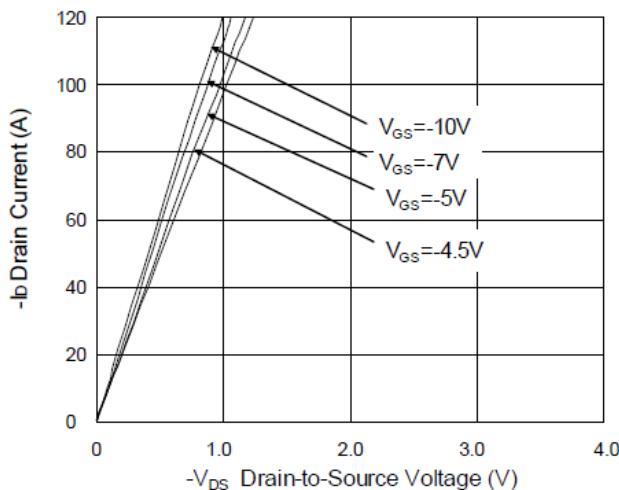


Fig.1 Typical Output Characteristics

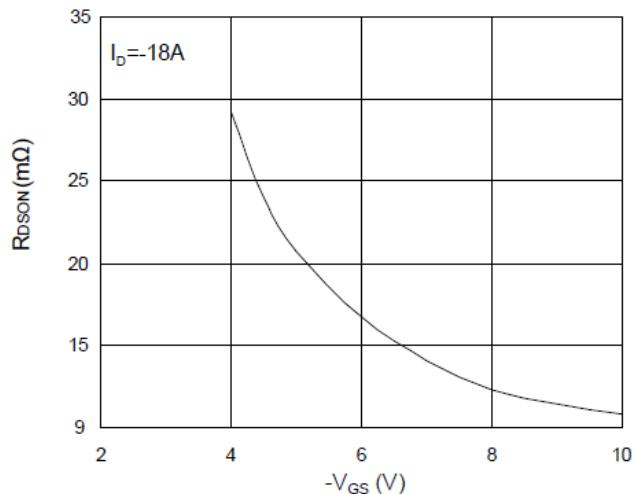


Fig.2 On-Resistance vs G-S Voltage

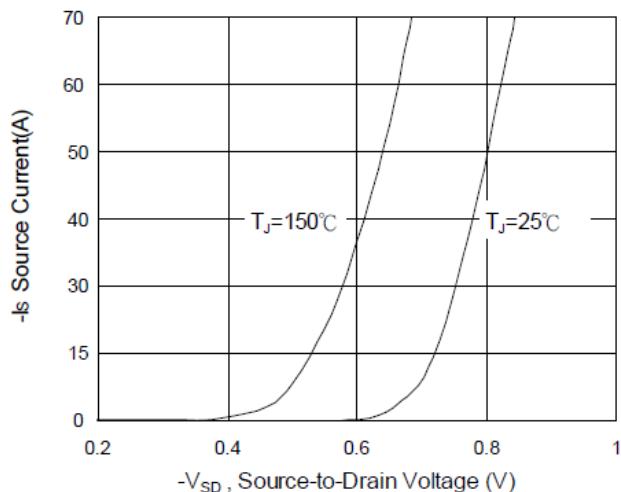


Fig.3 Source Drain Forward Characteristics

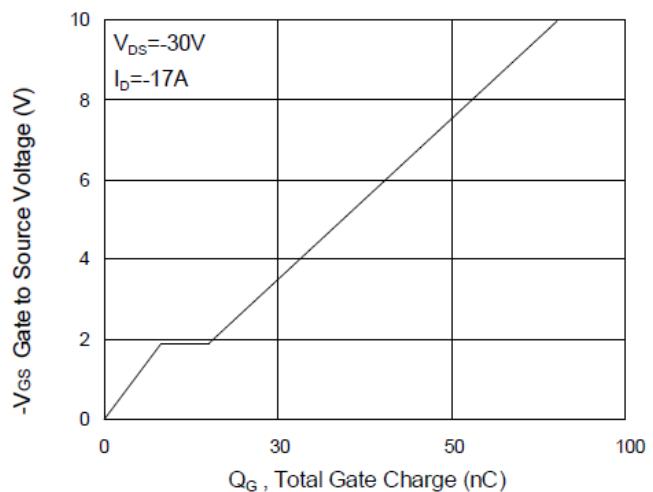


Fig.4 Gate-Charge Characteristics

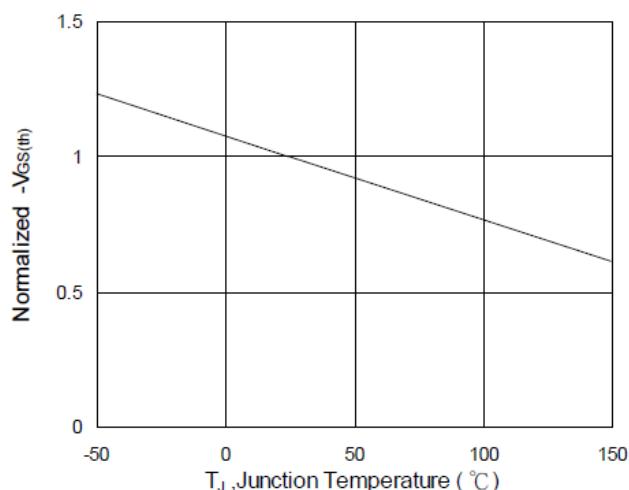


Fig.5 Normalized $V_{GS(th)}$ vs T_J

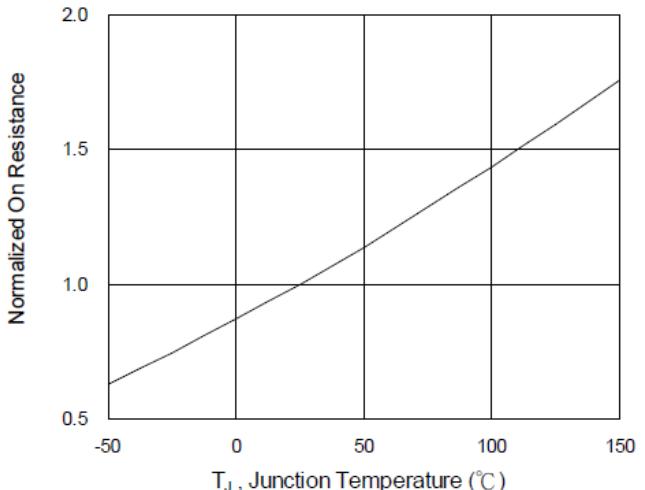


Fig.6 Normalized $R_{DS(on)}$ vs T_J

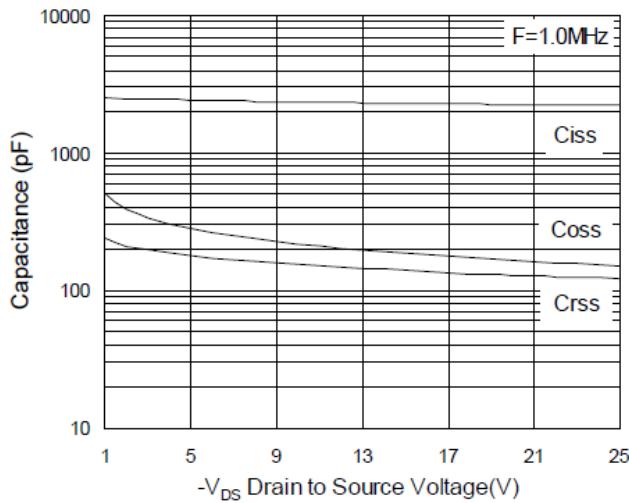


Fig.7 Capacitance

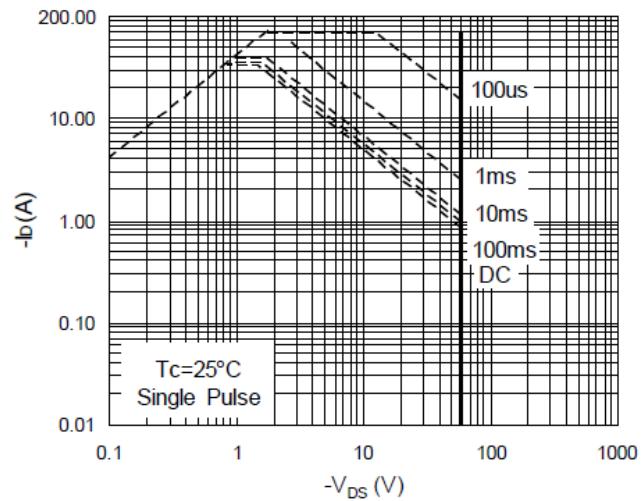


Fig.8 Safe Operating Area

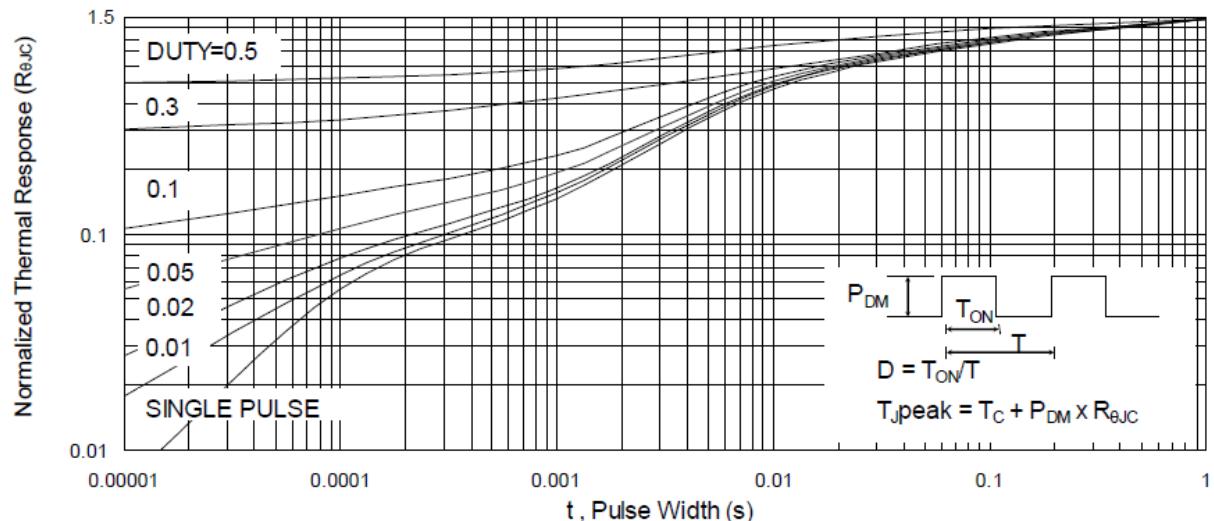


Fig.9 Normalized Maximum Transient Thermal Impedance

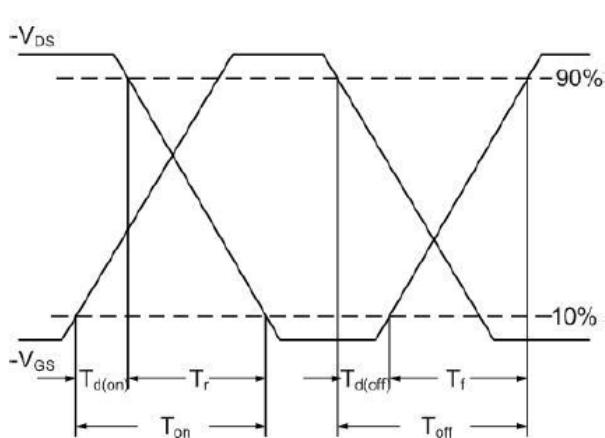


Fig.10 Switching Time Waveform

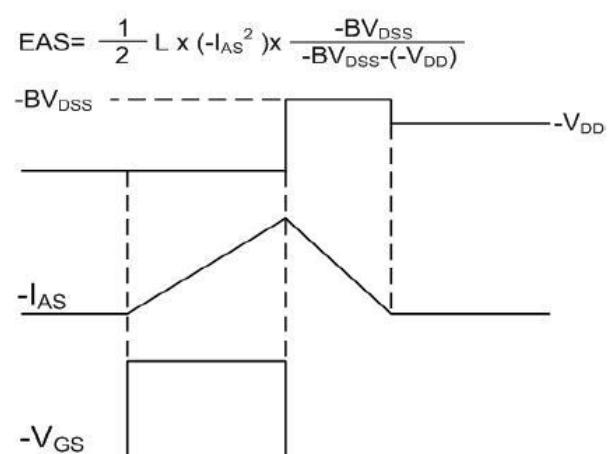
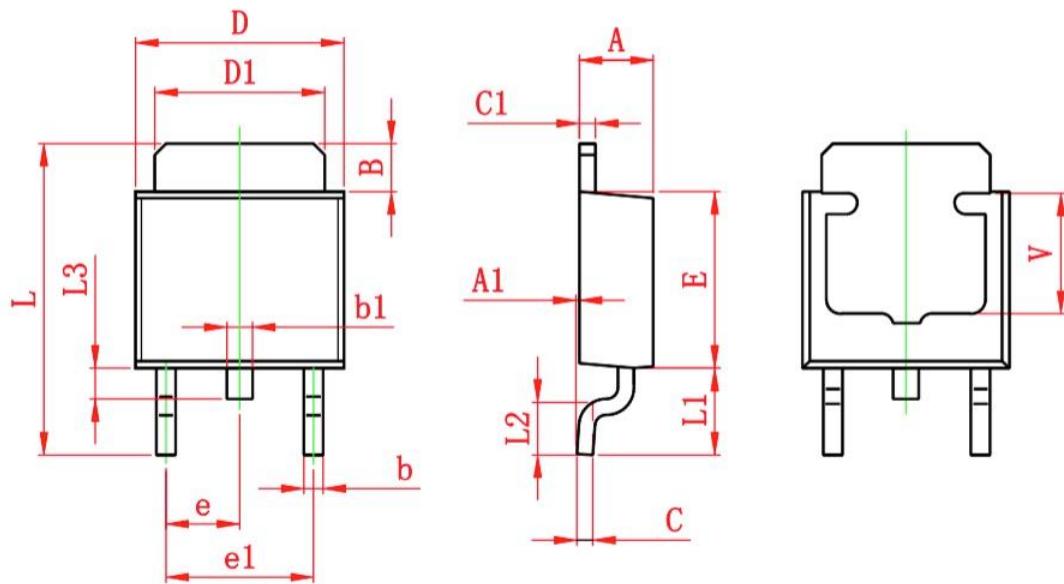


Fig.11 Unclamped Inductive Waveform



Ordering Information

Part Number	Package code	Packaging
HSU6117	TO252-2	2500/Tape&Reel



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
B	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
c	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	6.350	6.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.400	5.700	0.213	0.224
e	2.300 TYP.		0.091 TYP.	
e1	4.500	4.700	0.177	0.185
L	9.500	9.900	0.374	0.390
L1	2.550	2.900	0.100	0.114
L2	1.400	1.780	0.055	0.070
L3	0.600	0.900	0.024	0.035
V	3.800 REF.		0.150 REF.	